



How difficult is to get a JIT right?

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Quick About Me: Guille

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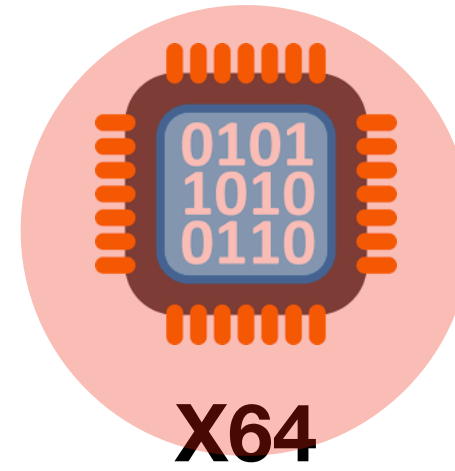
- Pronounced *gife* (guichet in FR, ~ghisheh in EN?)
- **Now:** Researcher at Inria - Lille
- Pharo Contributor since ~2010

- **Keywords:** compilers, testing, test generation
- **Interests:** tooling, benchmarking, 日本語, board games, batman, concurrency

If any of that interests you, come talk to me!



Debugging Assembly Code



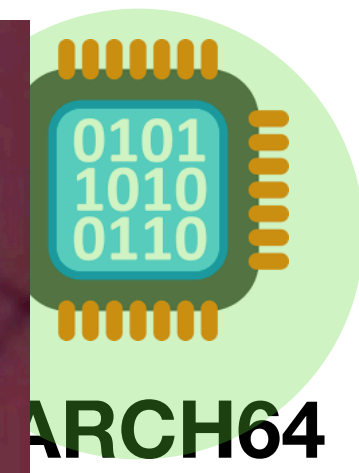
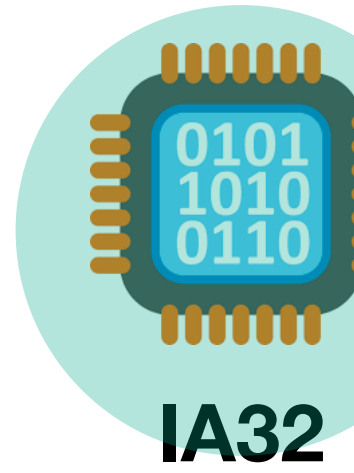
Address	ASM	Bytes	Name	Machine Alia	Smalltalk Ali	Value
16r1000000	mov esi, dword #['16r8B' '16r		eip			'16r1002000'
16r1000004	mov ecx, esi #['16r89' '16r		eax			'16r1001FB8'
16r1000006	test esi, 1 #['16rF7' '16r		ebx			'16r7FFFFFFDA'
16r100000C	je 12 #['16r74' '16r		ecx			'16rFFFFFFDE'
16r100000E	sub ecx, 1 #['16r83' '16r		edx			'16rFFFFFFDE'
16r1000011	add ecx, edx #['16r3' '16rC		esp			'16rF001FF4'
16r1000013	jo 5 #['16r70' '16r		ebp			'16rF002000'
16r1000015	mov edx, ecx #['16r89' '16r		esi			'16rFFFFFFEE'
16r1000017	ret 4 #['16rC2' '16r		edi			'16r0'
16r100001A	int3 #['16rCC']					
16r100001B	add byte ptr [e#['16r0' '16r0'					
16r100001D	add byte ptr [e#['16r0' '16r0'					
16r100001F	add byte ptr [e#['16r0' '16r0'					
16r1000021	add byte ptr [e#['16r0' '16r0'					
16r1000023	add byte ptr [e#['16r0' '16r0'					
16r1000025	add byte ptr [e#['16r0' '16r0'					
16r1000027	add byte ptr [e#['16r0' '16r0'					
16r1000029	add byte ptr [e#['16r0' '16r0'					
16r100002B	add byte ptr [e#['16r0' '16r0'					
16r100002D	add byte ptr [e#['16r0' '16r0'					
16r100002F	add byte ptr [e#['16r0' '16r0'					
16r1000031	add byte ptr [e#['16r0' '16r0'					
16r1000033	add byte ptr [e#['16r0' '16r0'					
16r1000035	add byte ptr [e#['16r0' '16r0'					
16r1000037	add byte ptr [e#['16r0' '16r0'					

Address	ASM	Bytes	Name	Machine Alia	Smalltalk Ali	Value
16r1000000	mov rdi, qword #['16r48' '16r		rip			'16r1002000'
16r1000005	mov rcx, rdi #['16r48' '16r		rax			'16r1001FB0'
16r1000008	test dil, 1 #['16r48' '16r		rbx	baseRegister		'16r7FFFFFFFI'
16r100000C	je 15 #['16r74' '16r		rcx	classRegister		'16rFFFFFFFI'
16r100000E	sub rcx, 1 #['16r48' '16r		rdx	receiverRegist		'16rFFFFFFFI'
16r1000012	add rcx, rdx #['16r48' '16r		rsp			'16rF001FE8'
16r1000015	jo 6 #['16r70' '16r		rbp	framePointer		'16rF002000'
16r1000017	mov rdx, rcx #['16r48' '16r		r8			'16r0'
16r100001A	ret 8 #['16rC2' '16r		r9	sendNumberC		'16r0'
16r100001D	int3 #['16rCC']		r10			'16r0'
16r100001E	add byte ptr [r#['16r0' '16r0'		r11			'16r0'
16r1000020	pop rbx #['16r5B']		r12			'16r0'
16r1000021	ret #['16rC3']		rsi			'16r0'
16r1000022	int3 #['16rCC']		rdi			'16rFFFFFFFI'
16r1000023	int3 #['16rCC']					
16r1000024	int3 #['16rCC']					
16r1000025	int3 #['16rCC']					
16r1000026	int3 #['16rCC']					
16r1000027	int3 #['16rCC']					
16r1000028	add byte ptr [r#['16r0' '16r0'					
16r100002A	add byte ptr [r#['16r0' '16r0'					
16r100002C	add byte ptr [r#['16r0' '16r0'					
16r100002E	add byte ptr [r#['16r0' '16r0'					
16r1000030	add byte ptr [r#['16r0' '16r0'					
16r1000032	add byte ptr [r#['16r0' '16r0'					

Address	ASM	Bytes	Name	Machine Alia	Smalltalk Ali	Value
16r30000000	ldr x3, [x28] #['16r83' '16r:		lr			'16r1002000'
16r30000004	mov x22, x3 #['16rF6' '16r:		pc			'16r1002000'
16r30000008	tst x3, #0x1 #['16r7F' '16r		sp			'16r1001FC0'
16r3000000C	b.eq #28 #['16rE0' '16r		fp			'16r2800020'
16r30000010	subs x22, x22, #['16rD6' '16r		x28	vmStackPoin		'16r280001F'
16r30000014	adds x22, x23, #['16rF6' '16r:		x0			'16r0'
16r30000018	b.vs #16 #['16r86' '16r		x1			'16r7FFFFFFFI'
16r3000001C	mov x23, x22 #['16rF7' '16r:		x2			'16r0'
16r30000020	add x28, x28, #['16r9C' '16r:		x3			'16rFFFFFFFI'
16r30000024	ret #['16rC0' '16r:		x4			'16r0'
16r30000028	brk #0 #['16r0' '16r0'		x5			'16r0'
16r3000002C	nop #['16r1F' '16r:		x6			'16r0'
16r30000030	.inst undefinec#['16rF0' '16r:		x7			'16r0'
16r30000034	udf #0 #['16r0' '16r0'		x8			'16r0'
16r30000038	.inst undefinec#['16rF8' '16r:		x9			'16r0'
16r3000003C	udf #0 #['16r0' '16r0'		x10			'16r0'
16r30000040	udf #0 #['16r0' '16r0'		x11			'16r0'
16r30000044	udf #0 #['16r0' '16r0'		x12			'16r0'
16r30000048	udf #0 #['16r0' '16r0'		x16			'16r1001FF8'
16r3000004C	udf #0 #['16r0' '16r0'		x19			'16r0'
16r30000050	udf #0 #['16r0' '16r0'		x20			'16r0'
16r30000054	udf #0 #['16r0' '16r0'		x21			'16r0'
16r30000058	udf #0 #['16r0' '16r0'		x22	classRegister		'16rFFFFFFFI'
16r3000005C	udf #0 #['16r0' '16r0'		x23	receiverRegis		'16rFFFFFFFI'
16r30000060	udf #0 #['16r0' '16r0'		x24	baseRegister		'16r7FFFFFFFI'

Debugging Assembly Code

Without looking at it

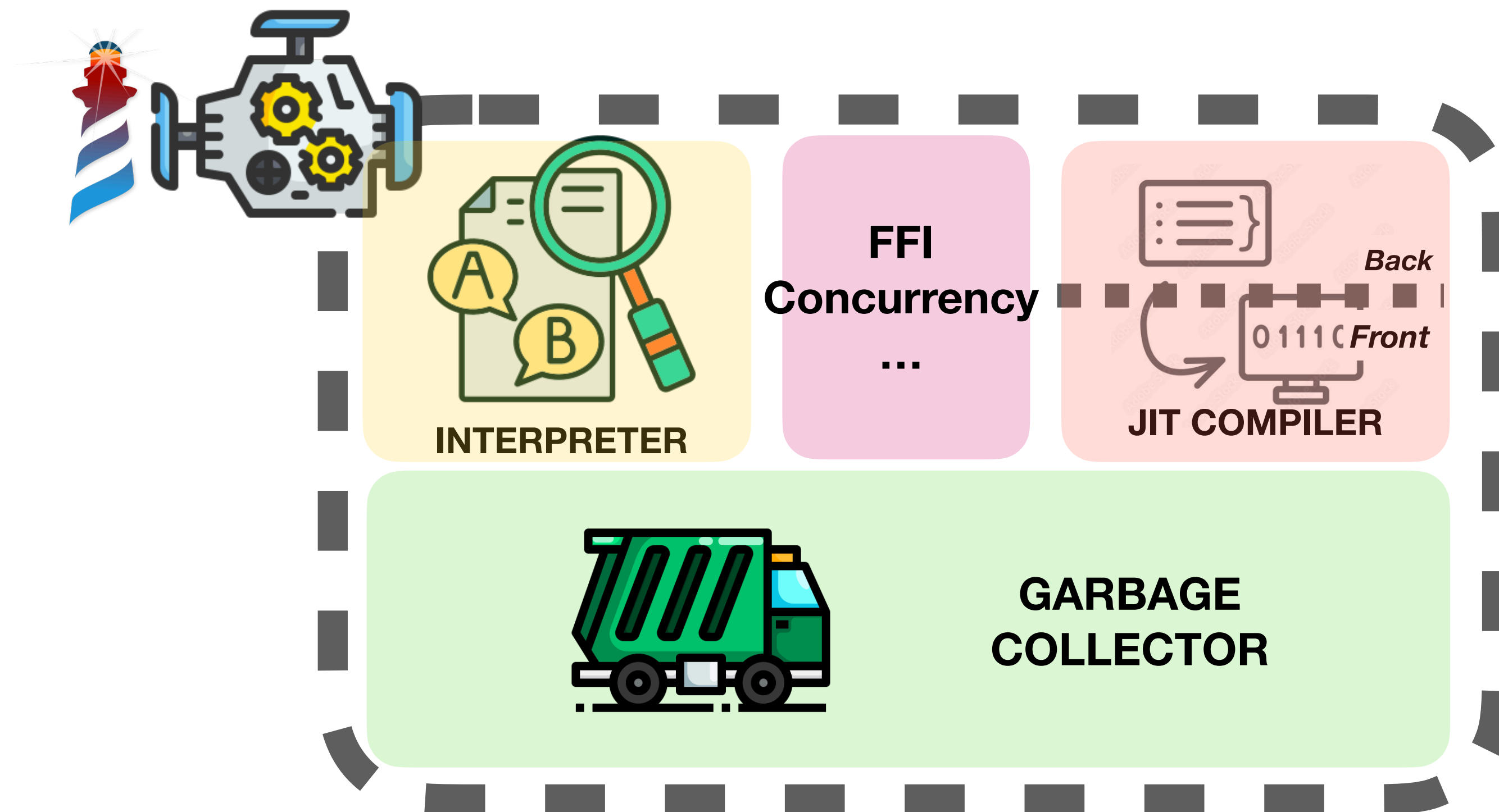


Address	ASM	Bytes	Name
16r10000000	mov esi, dword #['16r8B' '16r		eip
16r10000004	mov ecx, esi #['16r89' '16r		eax
16r10000006	test esi, 1 #['16rF7' '16r		ebx
16r1000000C	je 12 #['16r74' '16r		ecx
16r1000000E	sub ecx, 1 #['16r83' '16r		edx
16r10000011	add ecx, edx #['16r3' '16rC		esp
16r10000013	jo 5 #['16r70' '16r		ebp
16r10000015	mov edx, ecx #['16r89' '16r		esi
16r10000017	ret 4 #['16rC2' '16r		edi
16r1000001A	int3 #['16rCC']		
16r1000001B	add byte ptr [e#['16r0' '16r0'		
16r1000001D	add byte ptr [e#['16r0' '16r0'		
16r1000001F	add byte ptr [e#['16r0' '16r0'		
16r10000021	add byte ptr [e#['16r0' '16r0'		
16r10000023	add byte ptr [e#['16r0' '16r0'		
16r10000025	add byte ptr [e#['16r0' '16r0'		
16r10000027	add byte ptr [e#['16r0' '16r0'		
16r10000029	add byte ptr [e#['16r0' '16r0'		
16r1000002B	add byte ptr [e#['16r0' '16r0'		
16r1000002D	add byte ptr [e#['16r0' '16r0'		
16r1000002F	add byte ptr [e#['16r0' '16r0'		
16r10000031	add byte ptr [e#['16r0' '16r0'		
16r10000033	add byte ptr [e#['16r0' '16r0'		
16r10000035	add byte ptr [e#['16r0' '16r0'		
16r10000037	add byte ptr [e#['16r0' '16r0'		

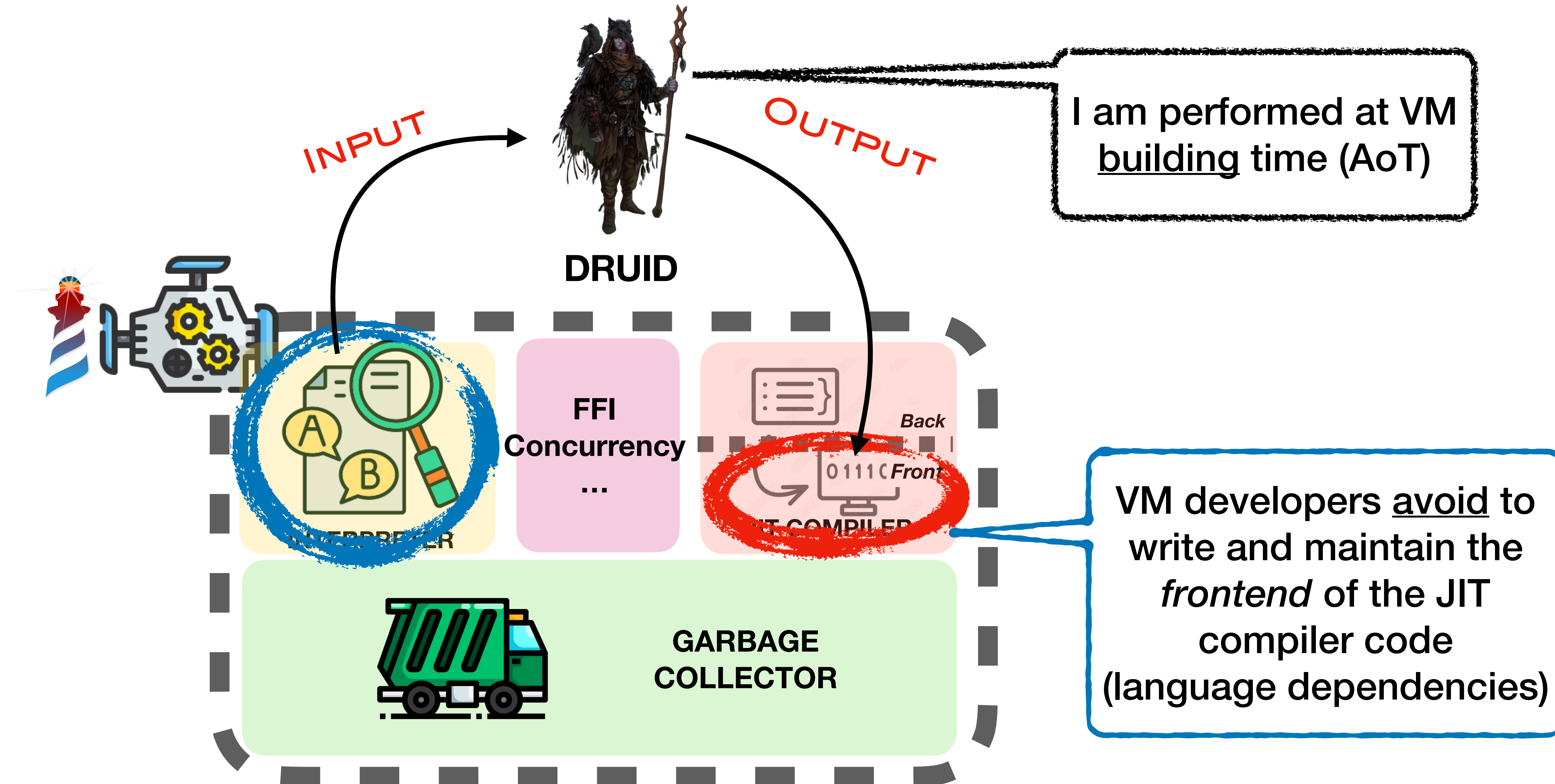
16r1000002E	add byte ptr [r#['16r0' '16r0'
16r10000030	add byte ptr [r#['16r0' '16r0'
16r10000032	add byte ptr [r#['16r0' '16r0'

Name	Machine Ali	Smalltalk Ali	Value
16r:	lr		'16r1002000
16r:	pc		'16r1002000
16r:	sp		'16r1001FC0
16r:	fp		'16r2800020
16r:	x28	vmStackPoin	'16r280001F
16r:	x0		'16r0'
16r:	x1		'16r7FFFFFFF
16r:	x2		'16r0'
16r:	x3		'16rFFFFFFF
16r:	x4		'16r0'
16r:	x5		'16r0'
16r:	x6		'16r0'
16r:	x7		'16r0'
16r:	x8		'16r0'
16r:	x9		'16r0'
16r:	x10		'16r0'
16r:	x11		'16r0'
16r:	x12		'16r0'
16r:	x16		'16r1001FF8
16r:	x19		'16r0'
16r:	x20		'16r0'
16r:	x21		'16r0'
16r:	x22	classRegister	'16rFFFFFFF
16r300000058	udf #0	receiverRegis	'16rFFFFFFF
16r30000005C	udf #0	baseRegister	'16r7FFFFFFF
16r300000060	udf #0		'16r7FFFFFFF

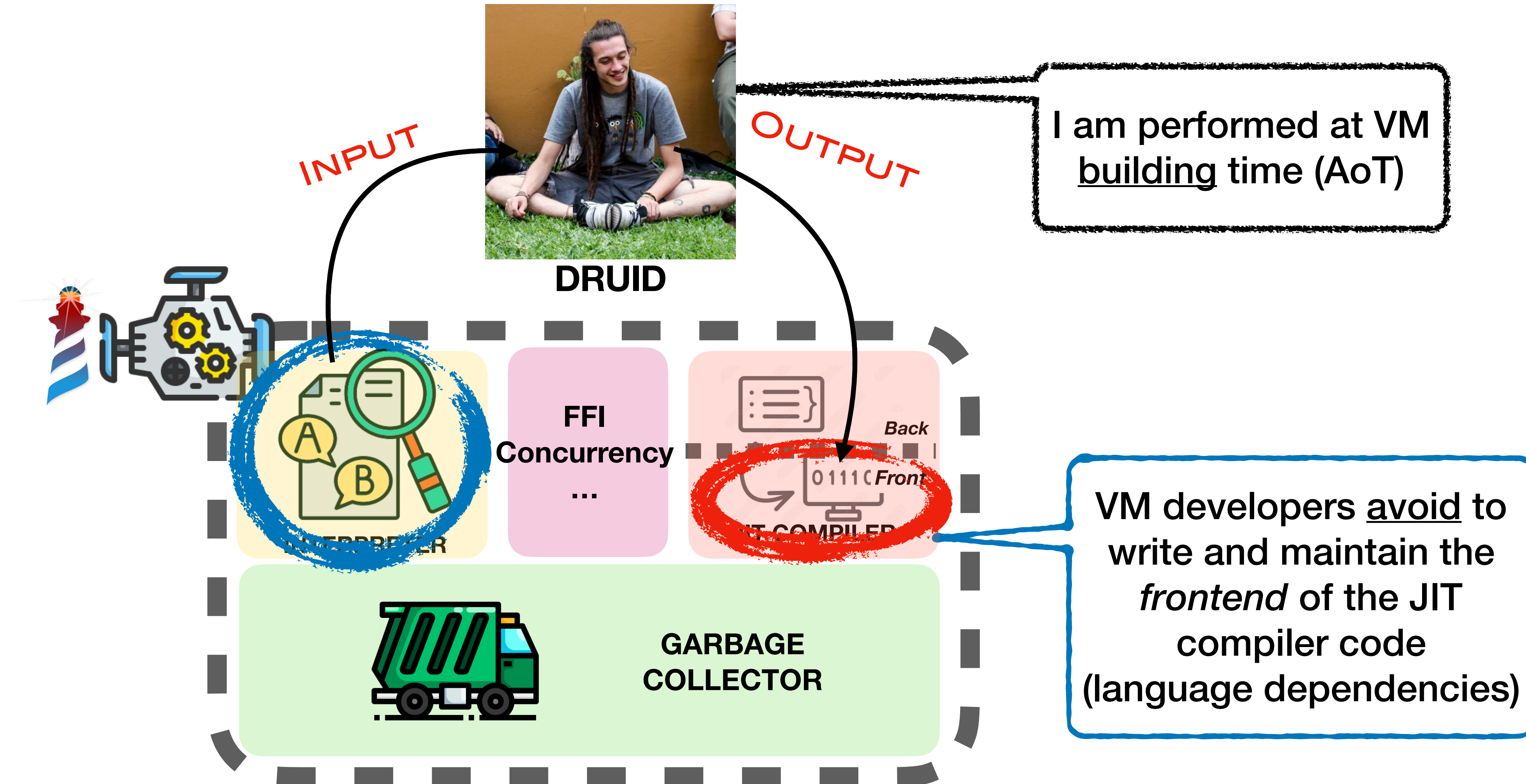
The Pharo VM



Context: Druid JIT compiler generation



Context: Druid JIT compiler generation



Druid by example: the addition primitive

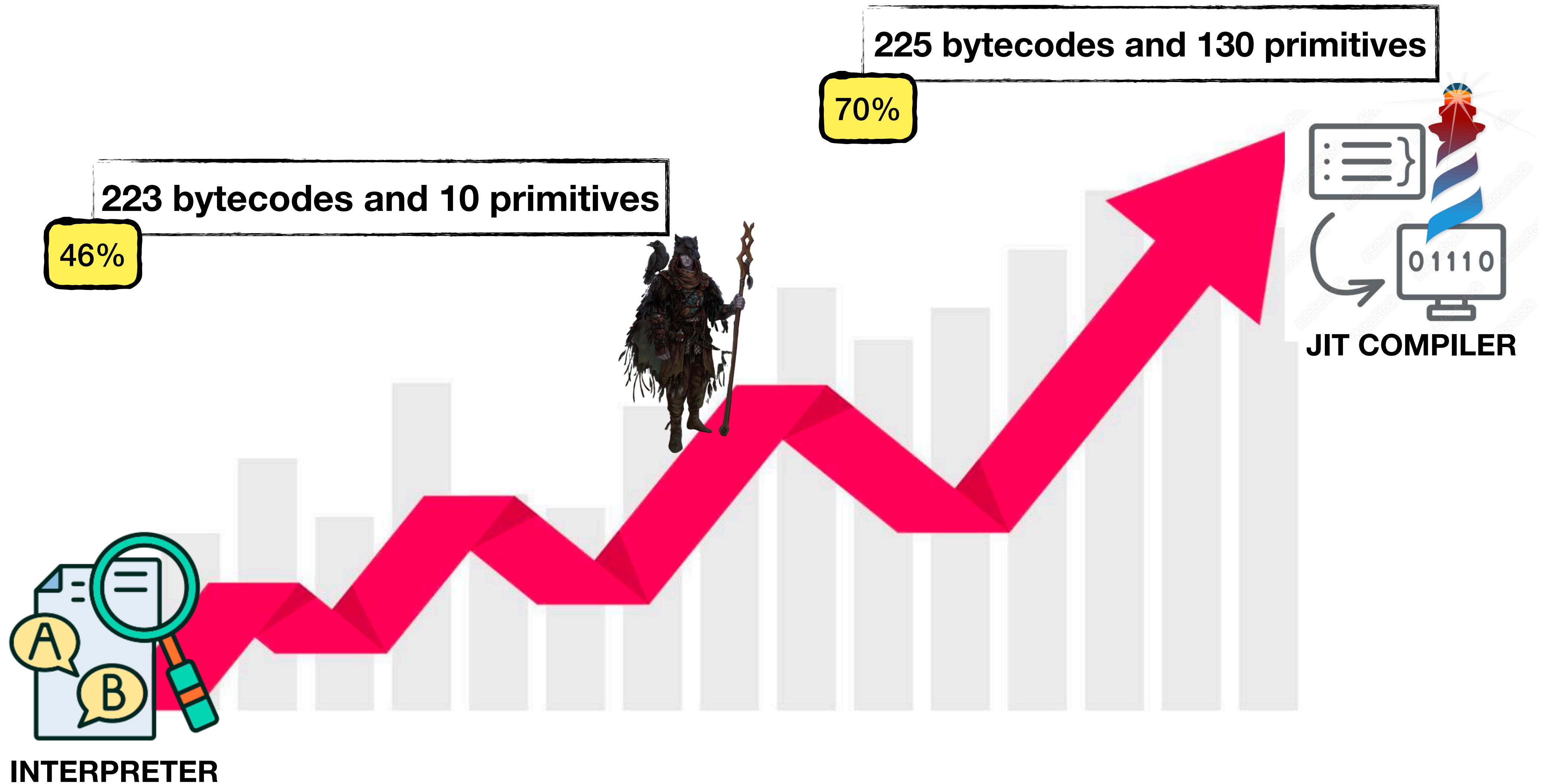
Interpreter

```
1 primitiveAdd
2   <numberOfArguments: 1>
3   <customisedReceiverFor: #smallInteger>
4
5   | maybeSmallInteger maybeSmallInteger2 result |
6
7   maybeSmallInteger := self stackValue: 0.
8   maybeSmallInteger2 := self stackValue: 1.
9
10  (objectMemory isIntegerObject: maybeSmallInteger)
11    ifFalse: [ ^ self primitiveFail ].
12  (objectMemory isIntegerObject: maybeSmallInteger2)
13    ifFalse: [ ^ self primitiveFail ].
14
15  "Check for overflow"
16  result := self
17    sumSmallInteger: maybeSmallInteger
18    withSmallInteger: maybeSmallInteger2
19    ifOverflow: [ ^ self primitiveFail ].
20
21  self pop: 2 thenPush: result
```

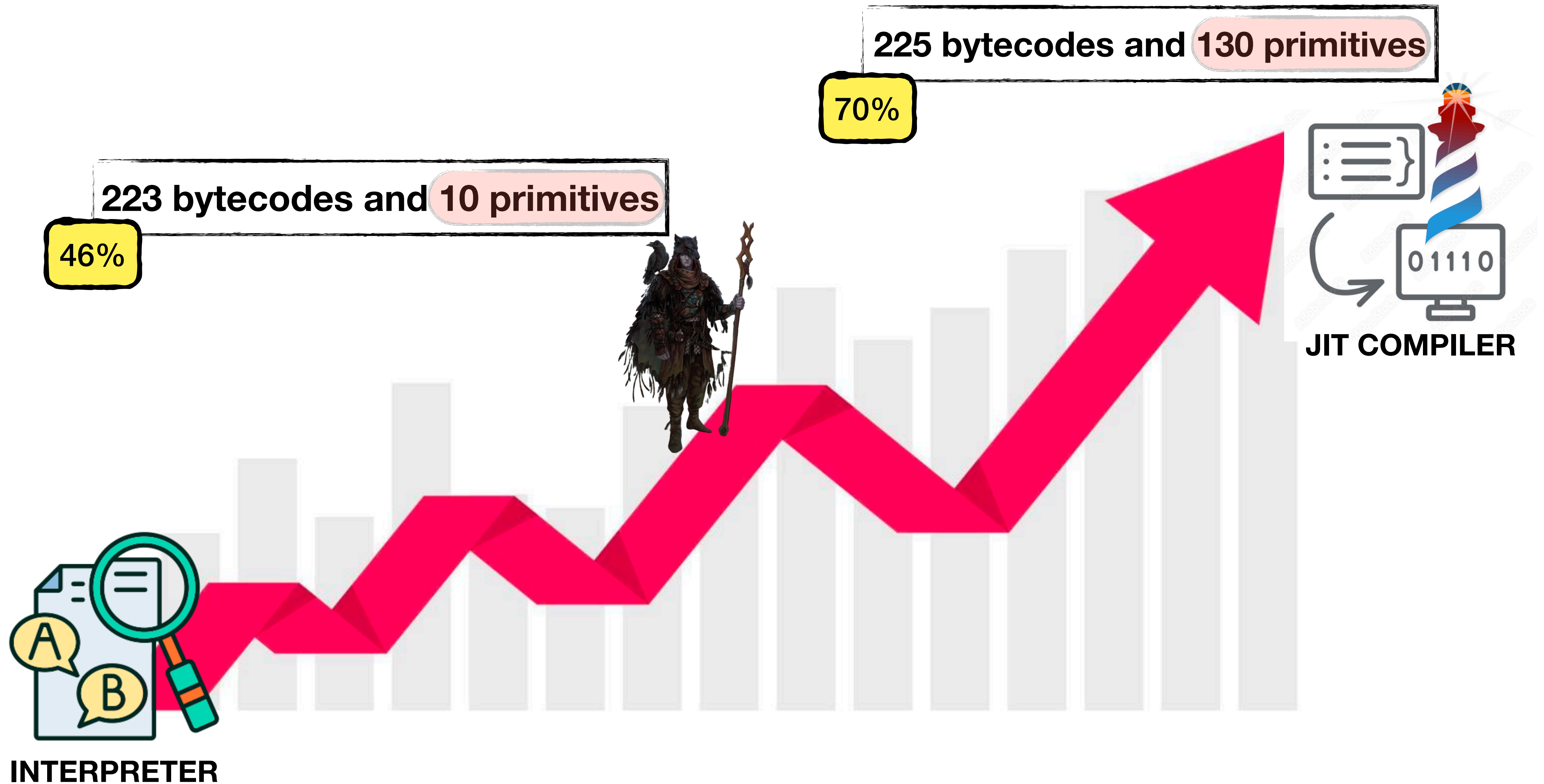
JIT Compiler

```
1 genPrimitiveAdd
2   | jumpNotSI jumpOvfl |
3   <var: #jumpNotSI type: #'AbstractInstruction *'>
4   <var: #jumpOvfl type: #'AbstractInstruction *'>
5   cogit mclassIsSmallInteger ifFalse:
6     [^UnimplementedPrimitive].
7
8   cogit genLoadArgAtDepth: 0 into: Arg0Reg.
9   cogit MoveR: Arg0Reg R: ClassReg.
10  jumpNotSI := self
11    genJumpNotSmallInteger: Arg0Reg scratchReg: TempReg.
12
13  self genRemoveSmallIntegerTagsInScratchReg: ClassReg.
14  cogit AddR: ReceiverResultReg R: ClassReg.
15  jumpOvfl := cogit JumpOverflow: 0.
16
17  cogit MoveR: ClassReg R: ReceiverResultReg.
18  cogit genPrimReturn.
19
20  jumpOvfl jmpTarget: (jumpNotSI jmpTarget: cogit Label).
21  ^CompletePrimitive
```


A Couple of Months Ago



A Couple of Months Ago



Generated JIT-Compiler

```
VariableNotDeclaredTest>>#testDescription [1453/1464]
WeakMessageSendTest>>#testCollectArguments [1454/1464]
WeakMessageSendTest(ClassTestCase)>>#testCoverage [1455/1464]
WeakMessageSendTest(ClassTestCase)>>#testMethodsOfTheClassShouldNotBeRepeatedInItsSuperclasses [1456/1464]
WeakMessageSendTest(ClassTestCase)>>#testNew [1457/1464]
WeakMessageSendTest>>#testNoArguments [1458/1464]
WeakMessageSendTest>>#testOneArgument [1459/1464]
WeakMessageSendTest>>#testOneArgumentWithGC [1460/1464]
WeakMessageSendTest>>#testReceiverWithGC [1461/1464]
WeakMessageSendTest(ClassTestCase)>>#testTraitExplicitRequirementMethodsMustBeImplementedInTheClassOrInASuperclass [1462/1464]
WeakMessageSendTest>>#testTwoArguments [1463/1464]
WeakMessageSendTest(ClassTestCase)>>#testUncategorizedMethods [1464/1464]
```

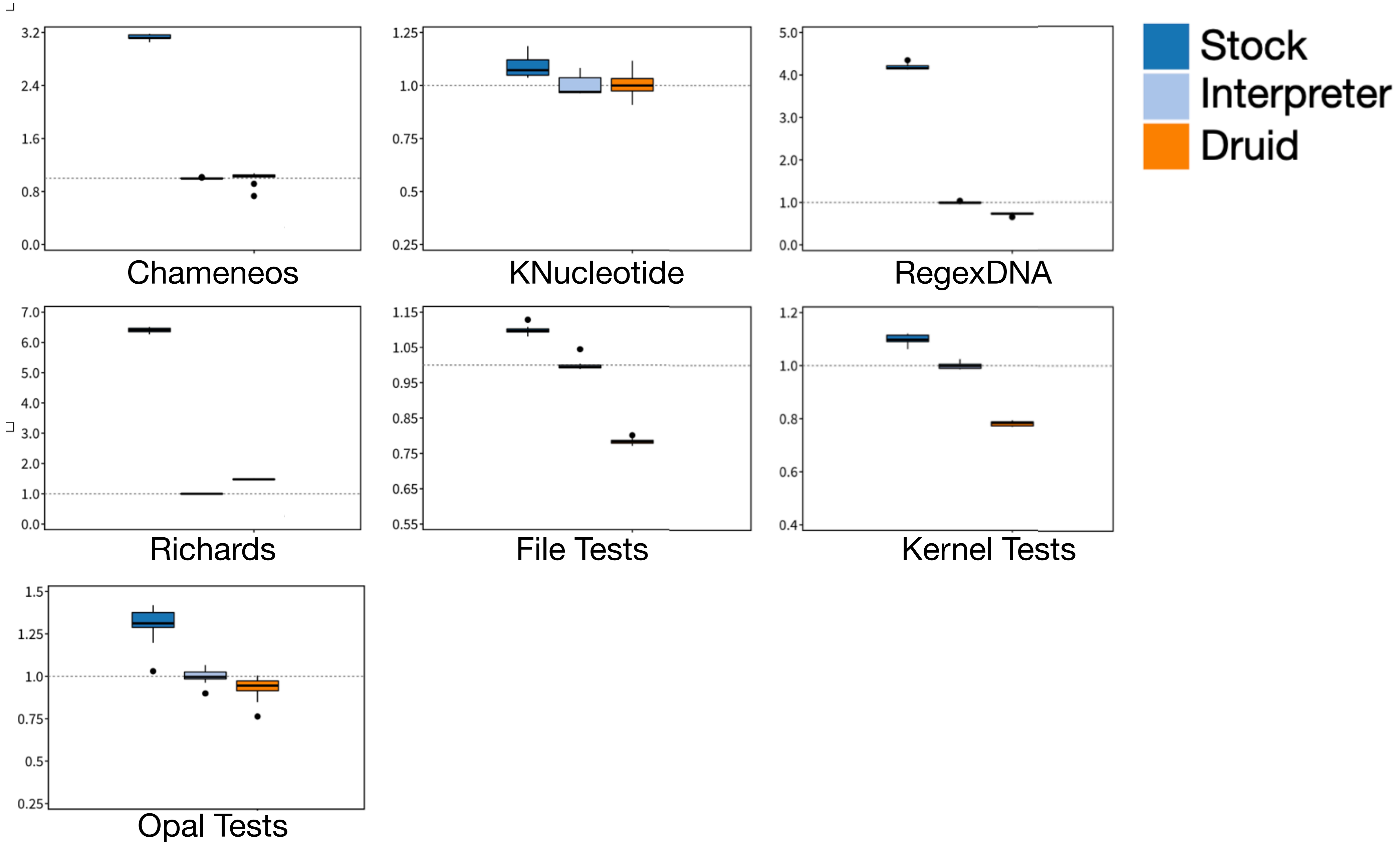
```
Finished running 1464 Tests
1890 run, 1890 passes, 0 failures, 0 errors.
```



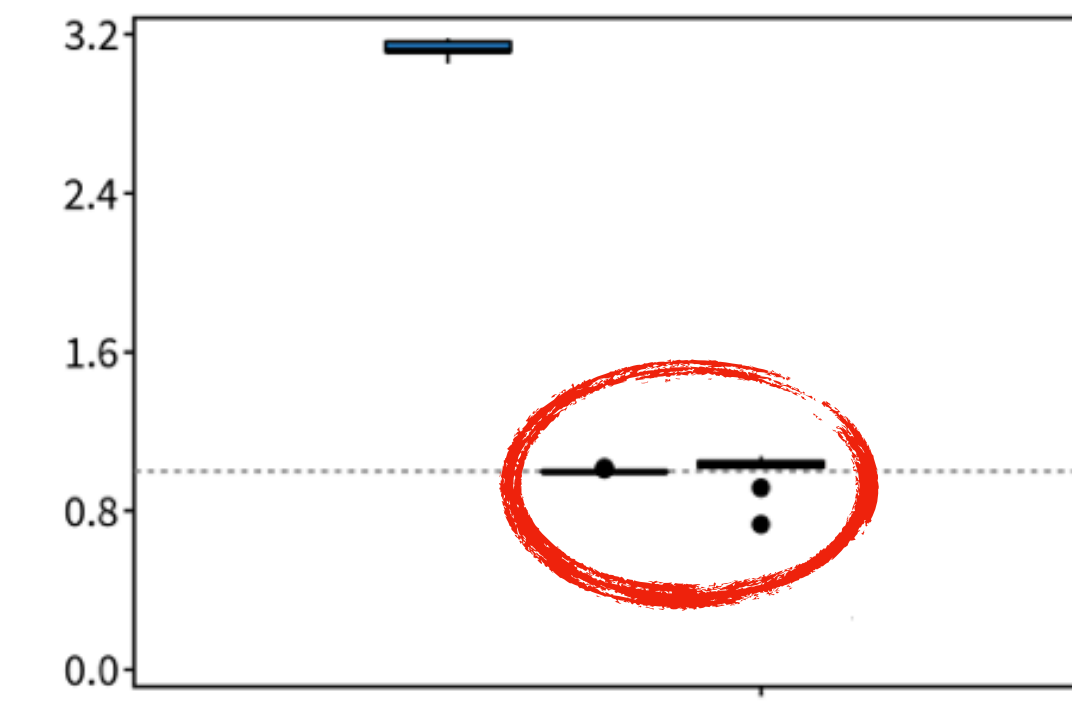
Generated JIT-Compiler

LOC per compiled primitive / bytecode	
Name	LOC
genBytecodePrim(29)	12
genDuplicateTopBytecode	30
genExtABytecode	13
genExtJumpIfFalse	78
genExtJumpIfTrue	78
genExtNonBytecode	13
...	
genReturnTopFromBlock	33
genReturnTopFromMethod	33
genSendLiteralSelectorBytecode(48)	12
genShortConditionalJump(16)	73
genShortUnconditionalJump(8)	12
genStoreAndPopTemporaryVariableBytecode(8)	37
Total	10895
Average	48.8

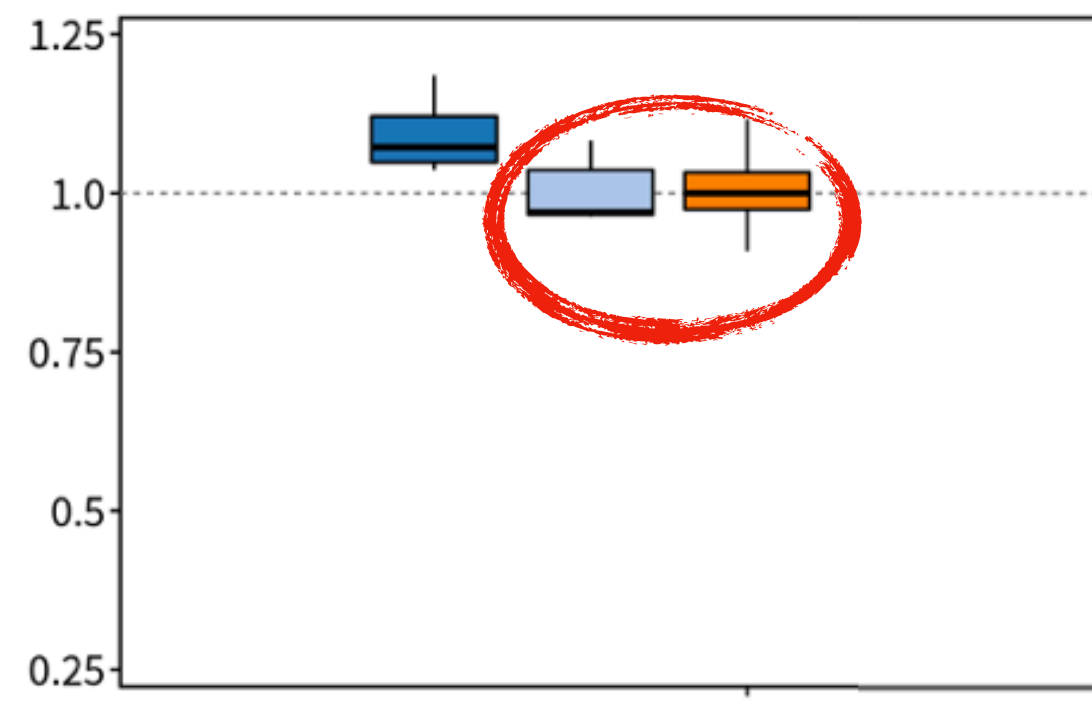
Some Initial benchmarks



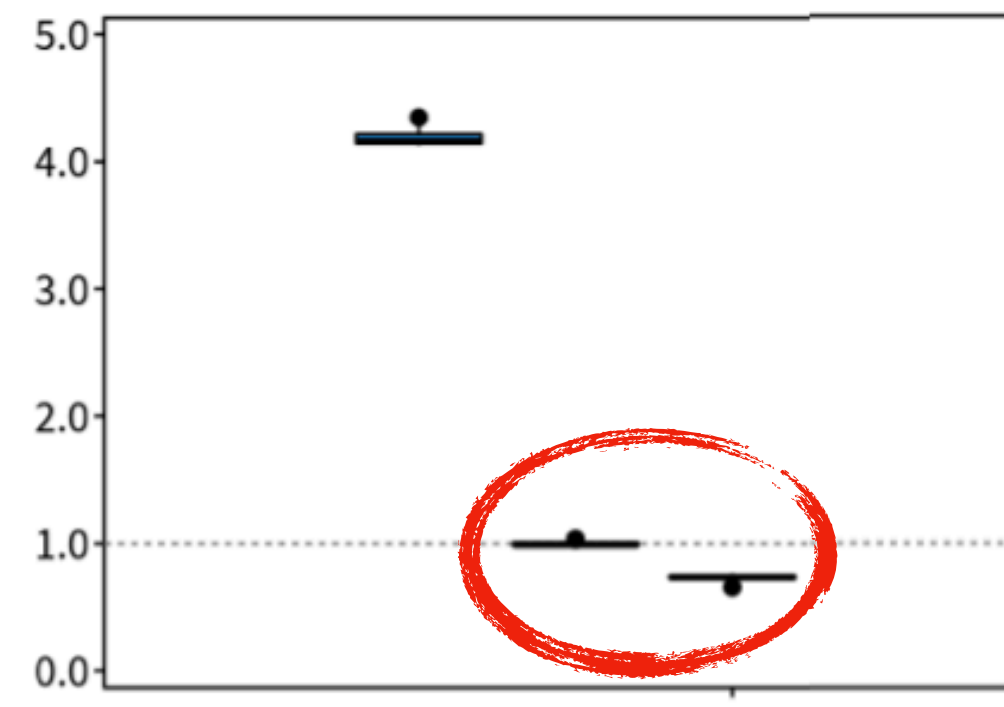
À la par with the interpreter



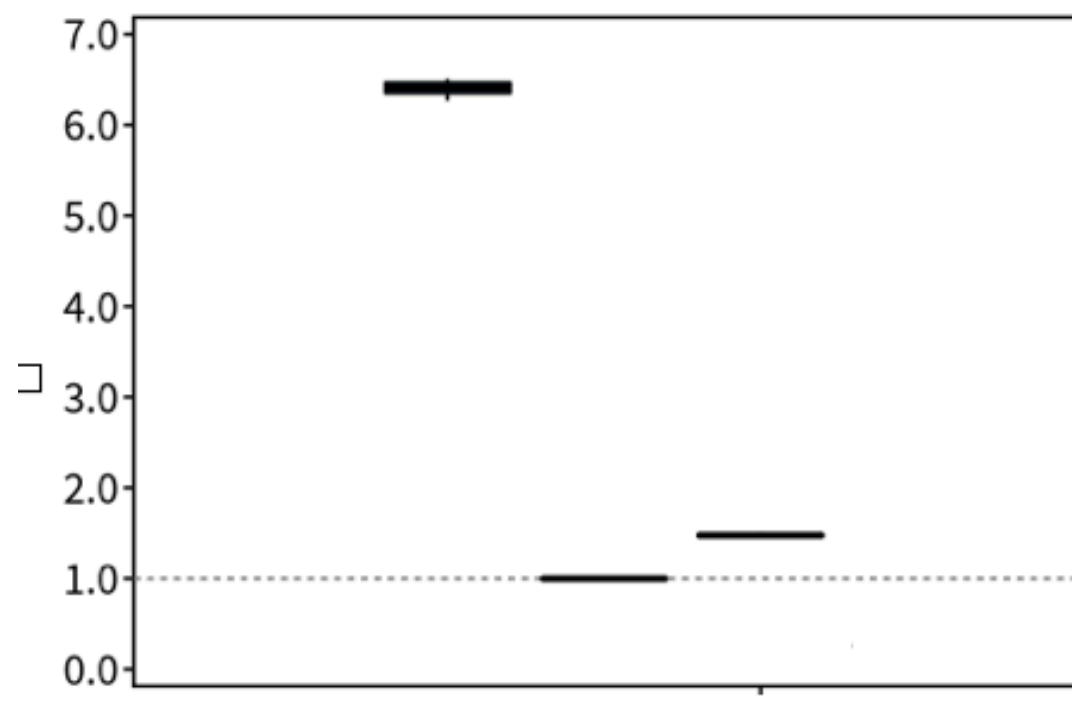
Chameneos



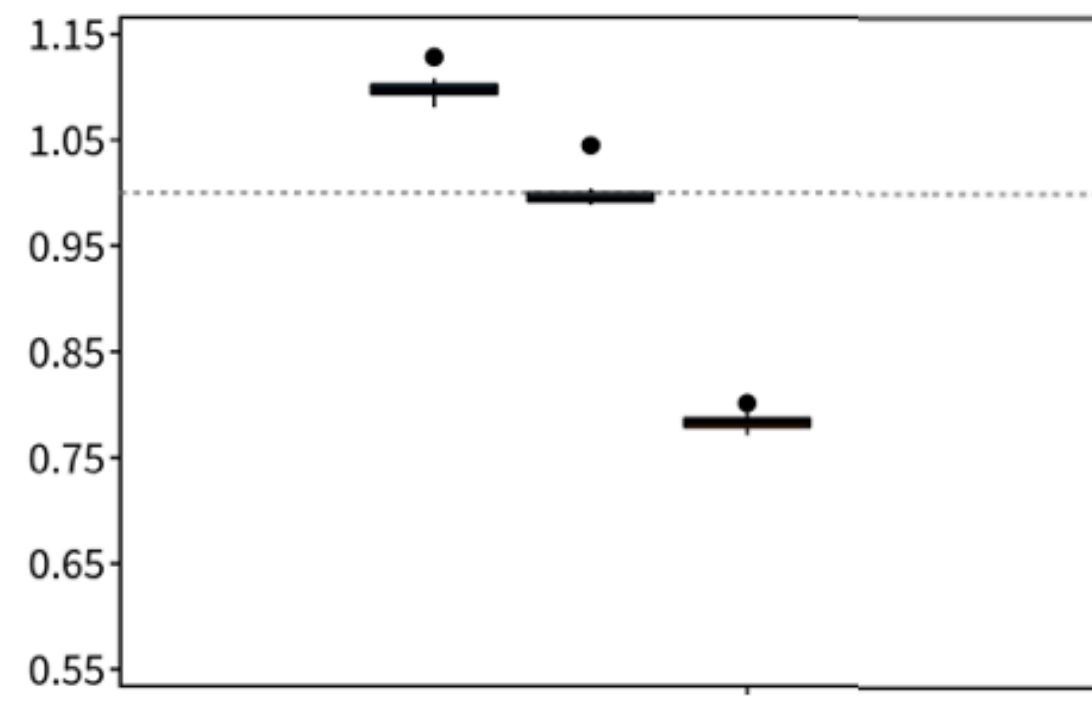
KNucleotide



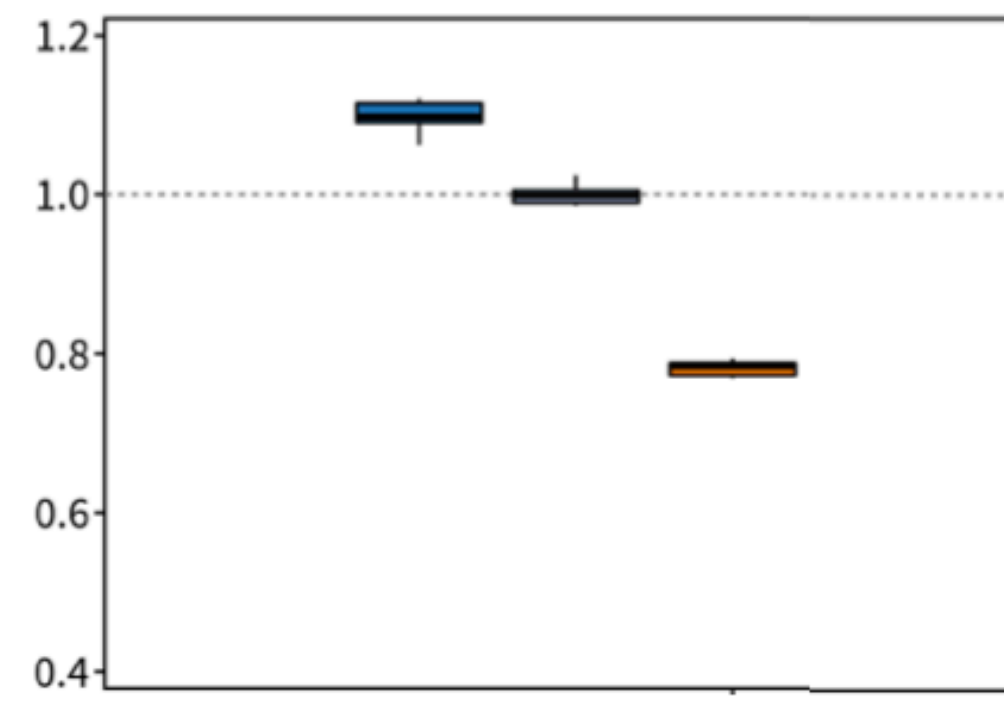
RegexDNA



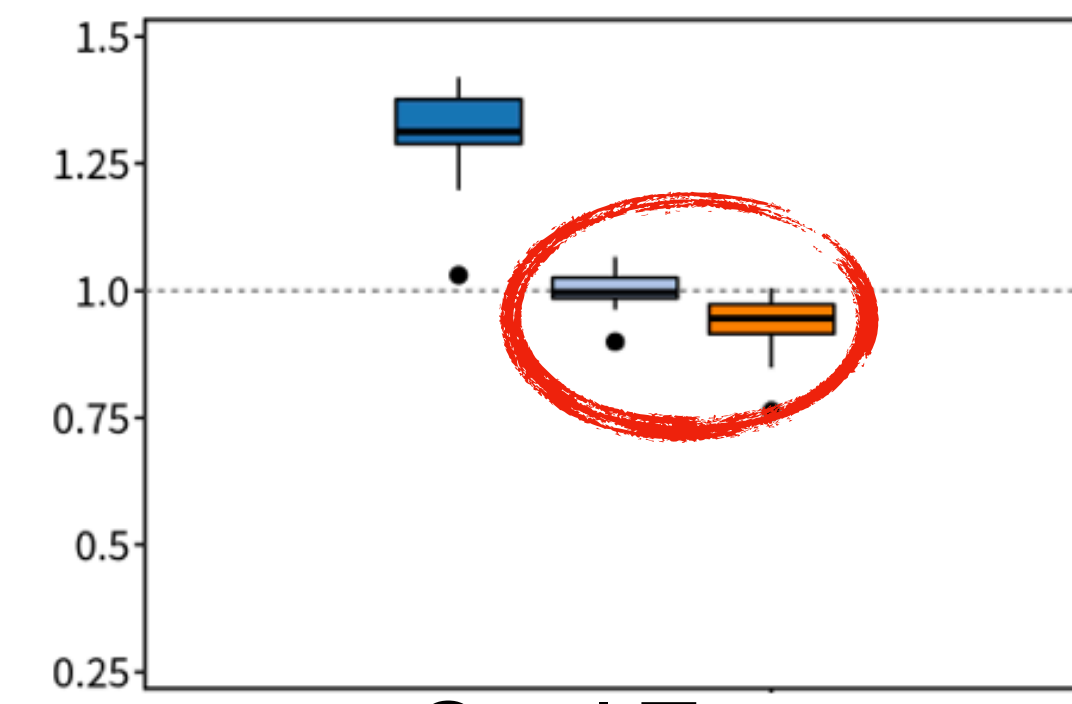
Richards



File Tests



Kernel Tests

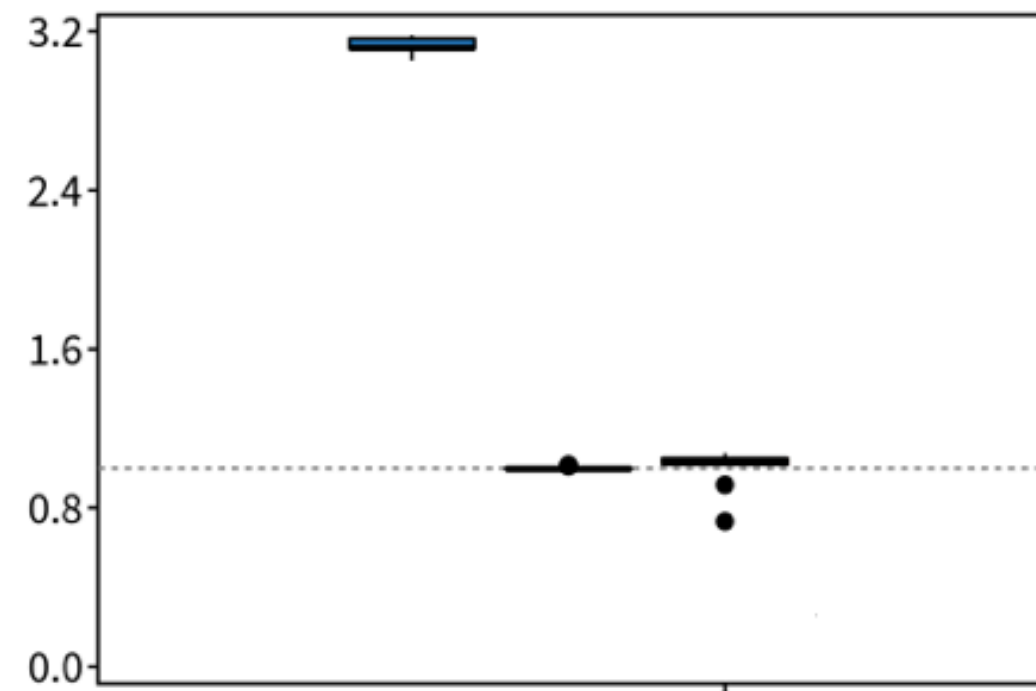


Opal Tests

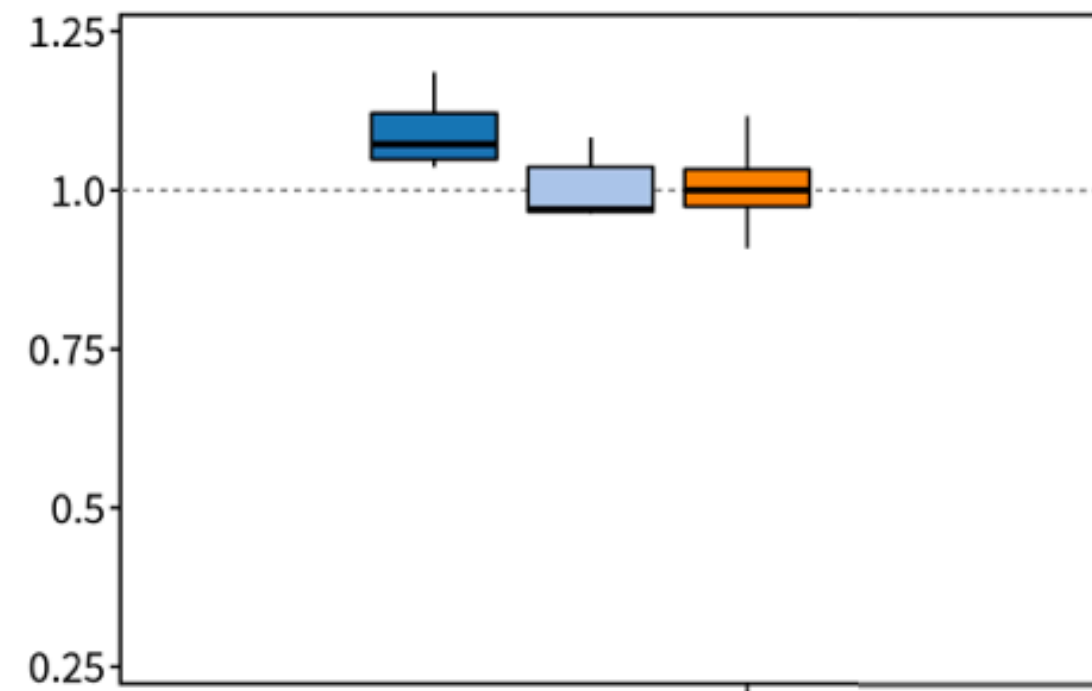


Slightly faster?

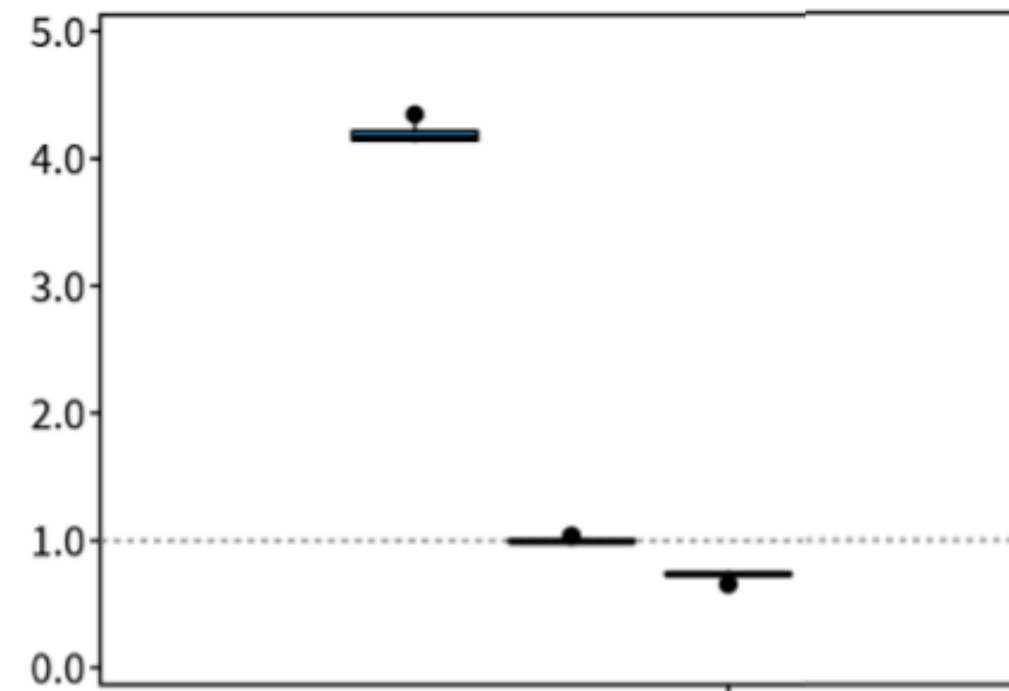
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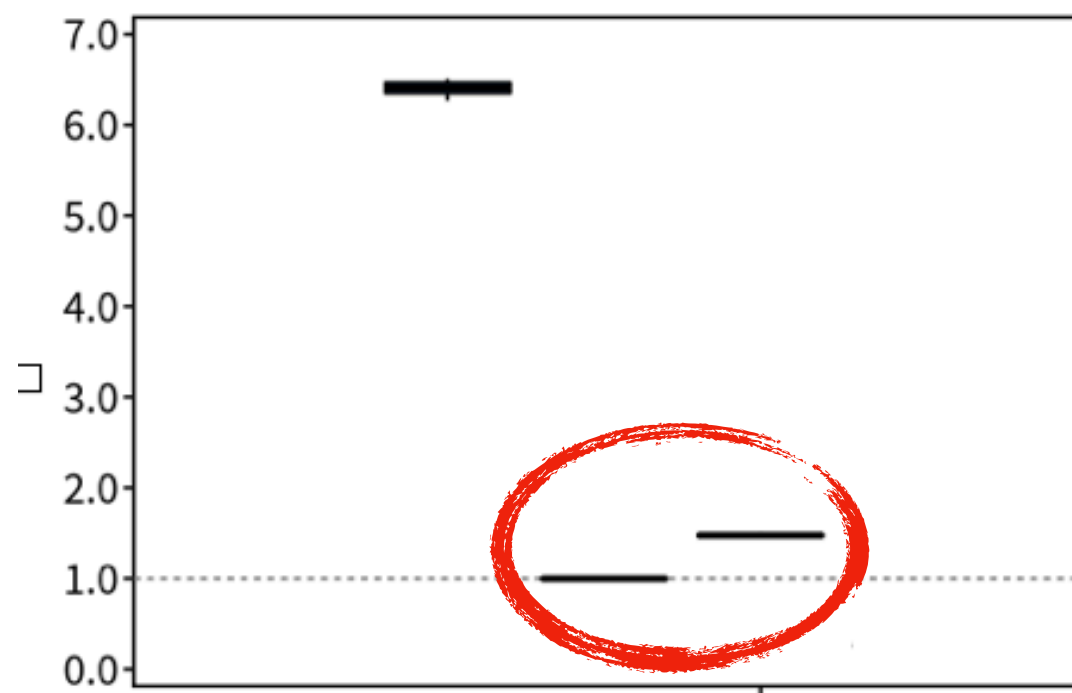
Chameneos



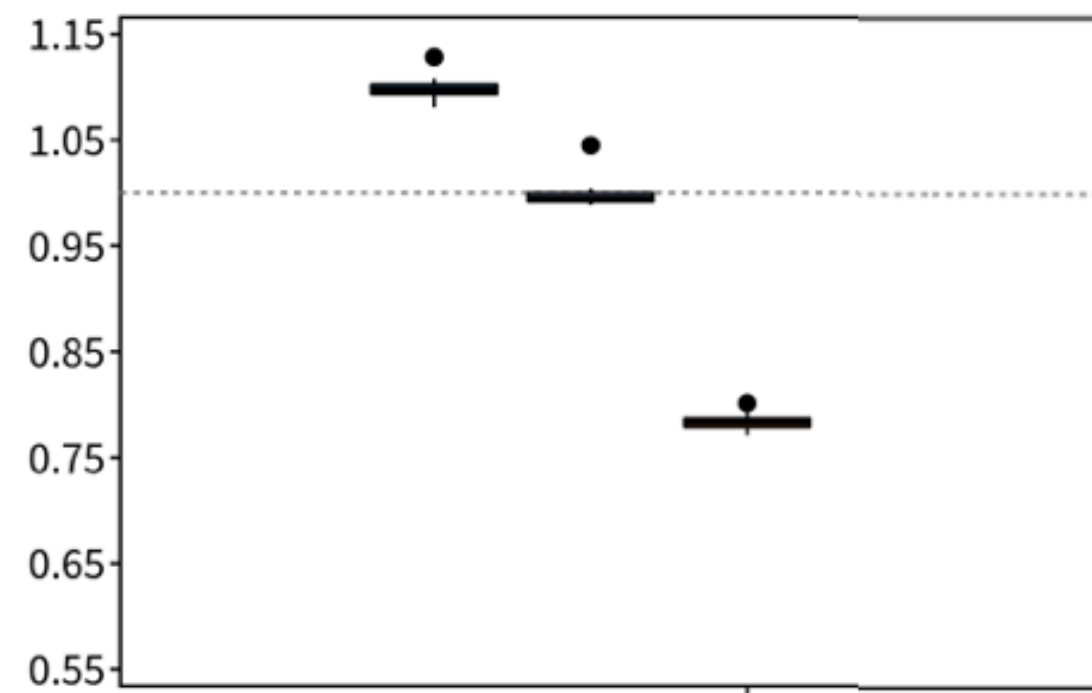
KNucleotide



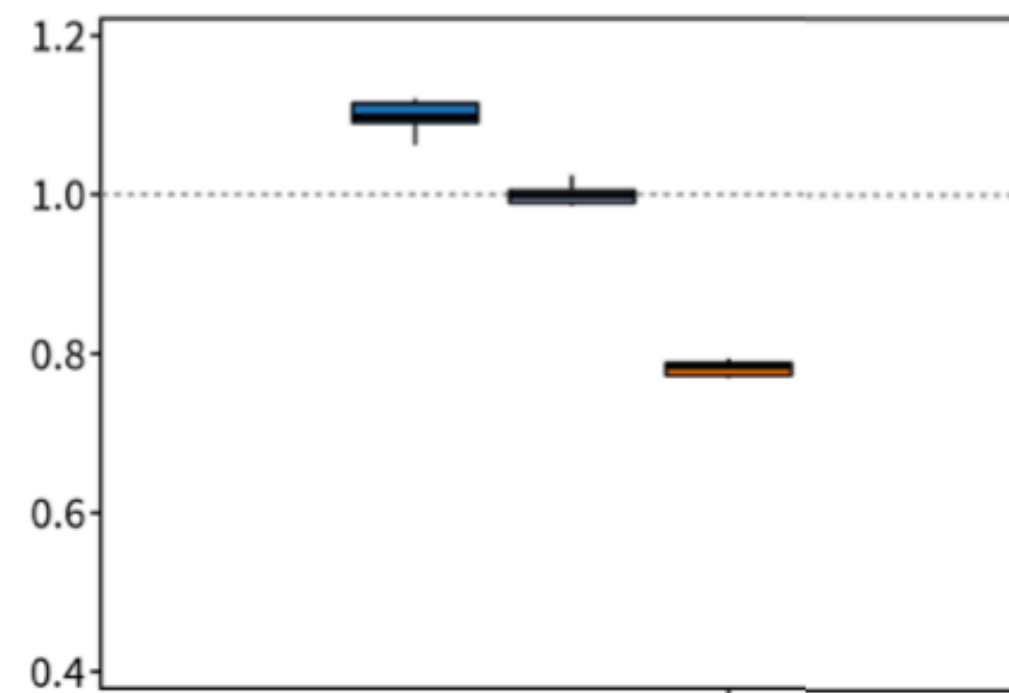
RegexDNA



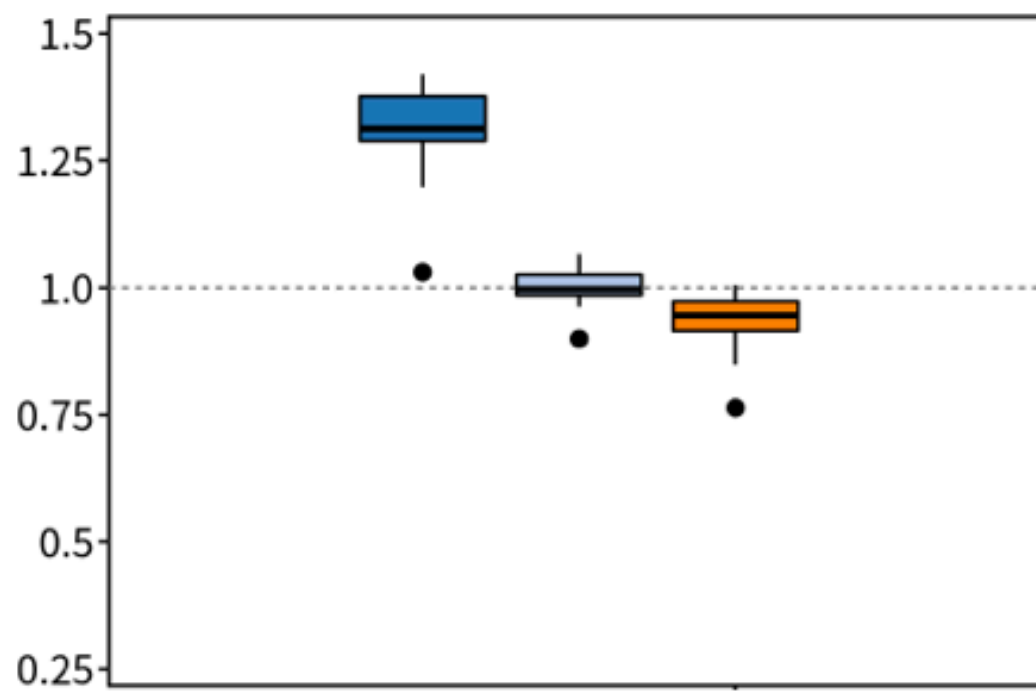
Richards



File Tests



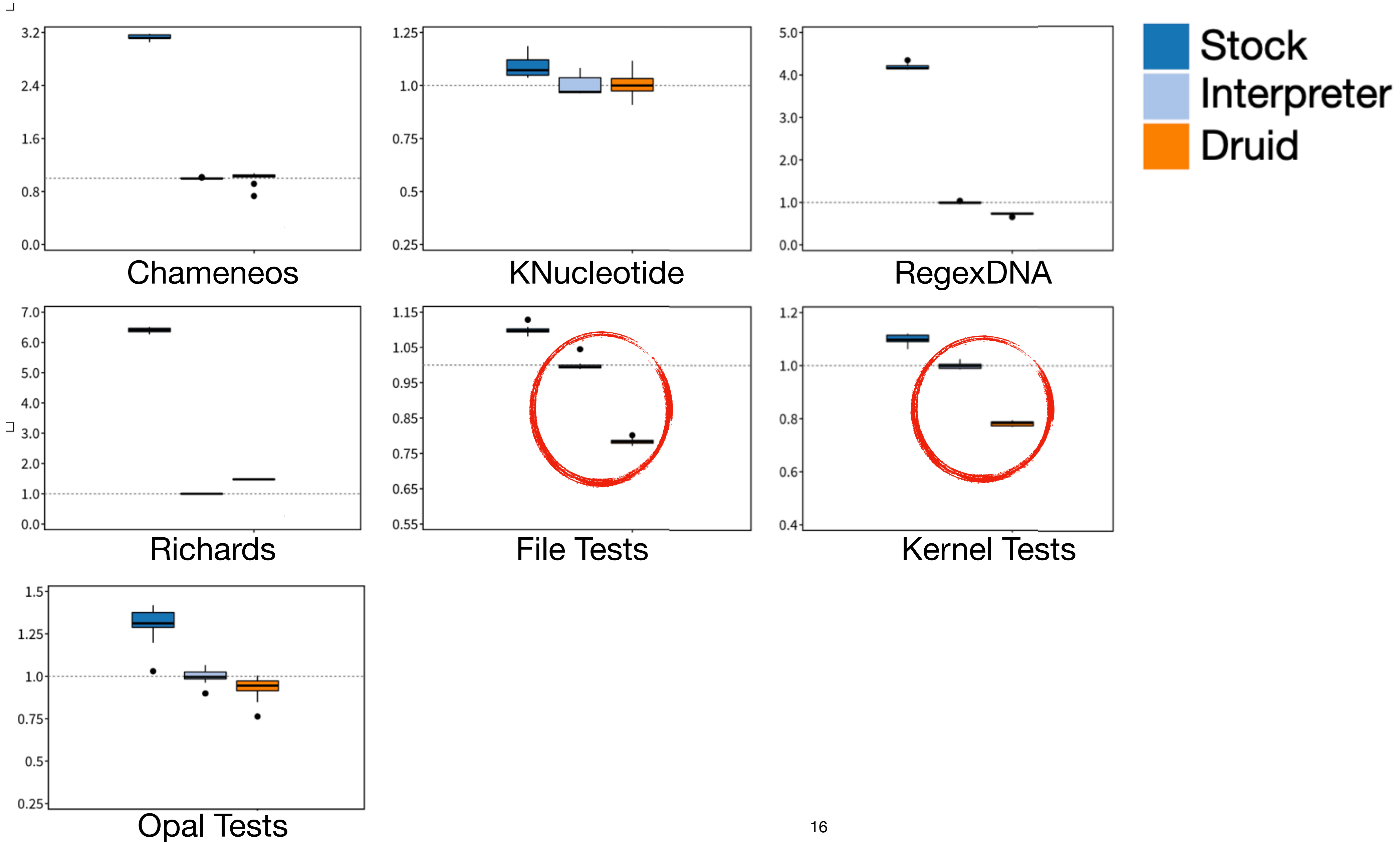
Kernel Tests



Opal Tests



And much slower too!



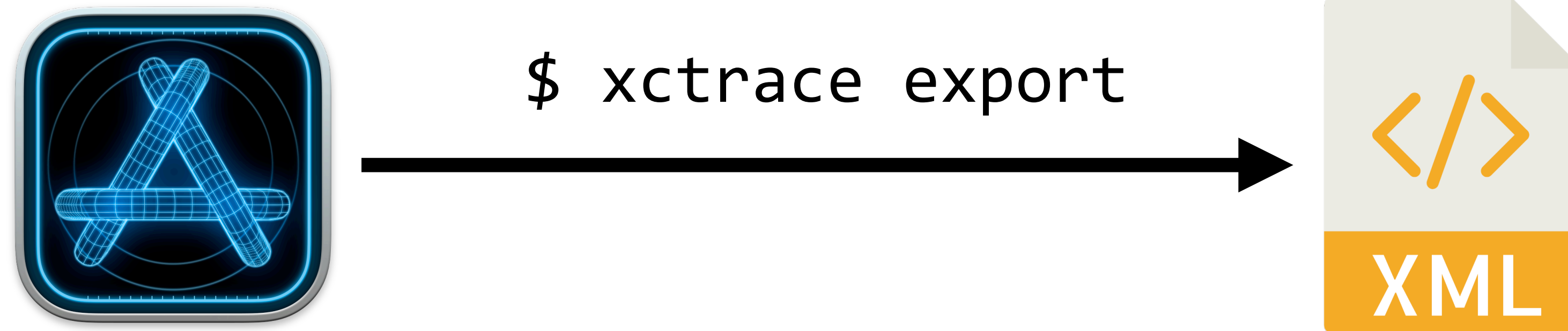
Where does the time go?

The screenshot shows the Xcode Instruments interface for a process named 'Pharo'. The 'Time Profiler' instrument is active, displaying a CPU usage graph at the top. Below the graph is a table of symbols and their weights. The most significant entry is '0x3000017f7' with a weight of 3.33 s (47.6%).

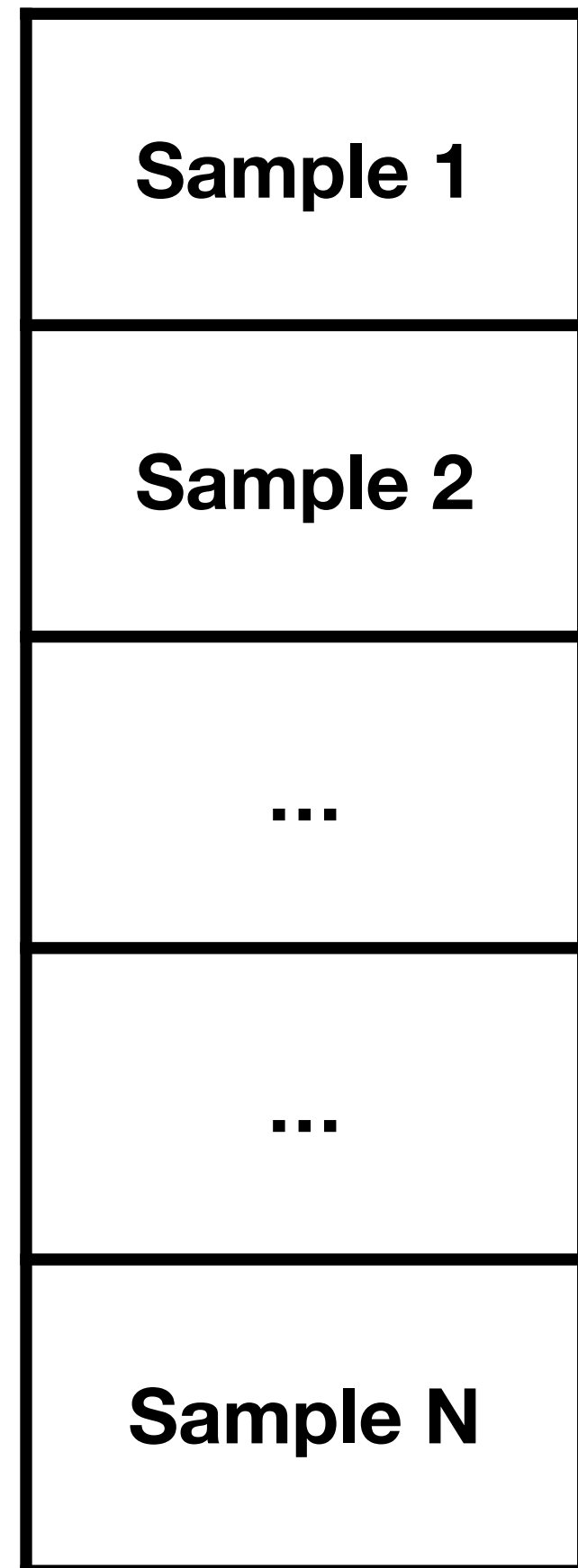
Weight	Self Weight	Symbol Name
7.00 s 100.0%	0 s	Pharo (59234)
3.64 s 51.9%	0 s	start dyld
3.63 s 51.8%	0 s	vm_main libPharoVMCore.dylib
3.63 s 51.8%	0 s	vm_main_with_parameters libPharoVMCore.dylib
3.63 s 51.8%	0 s	runOnMainThread [inlined] libPharoVMCore.dylib
3.63 s 51.8%	0 s	runVMThread [inlined] libPharoVMCore.dylib
1.00 ms 0.0%	0 s	vm_parameters_parse libPharoVMCore.dylib
7.00 ms 0.1%	0 s	dyld4::prepare(dyld4::APIs&, dyld3::MachOAnalyzer const*) dyld
3.33 s 47.6%	0 s	0x3000017f7
932.00 ms 13.3%	0 s	> 0x100028cd408
650.00 ms 9.2%	0 s	> 0x300001837
428.00 ms 6.1%	0 s	> 0x30000229b
188.00 ms 2.6%	0 s	> 0x30000219b
116.00 ms 1.6%	0 s	> 0x100003b30af
116.00 ms 1.6%	0 s	> 0x1000037795b
115.00 ms 1.6%	0 s	> 0x3001128df
80.00 ms 1.1%	0 s	> 0x100009186c5
47.00 ms 0.6%	0 s	> 0x1000268eb35
40.00 ms 0.5%	0 s	> 0x100003af11c
37.00 ms 0.5%	0 s	> 0x3000fd69f
25.00 ms 0.3%	0 s	> 0x3000787db
23.00 ms 0.3%	0 s	> 0x30003f863
21.00 ms 0.3%	0 s	> 0x100028bbd0b
19.00 ms 0.2%	0 s	> 0x10000406954
17.00 ms 0.2%	0 s	> 0x300002933
17.00 ms 0.2%	0 s	> 0x30000345b
16.00 ms 0.2%	0 s	> 0x1000040693c
15.00 ms 0.2%	0 s	> 0x300004213
15.00 ms 0.2%	0 s	> 0x10000406949
14.00 ms 0.2%	0 s	> 0x100012ed122
12.00 ms 0.1%	0 s	> 0x10000623afa
11.00 ms 0.1%	0 s	> 0x300077b53
10.00 ms 0.1%	0 s	> 0x3000026db
10.00 ms 0.1%	0 s	> 0x30000808f
9.00 ms 0.1%	0 s	> 0x3001243fb
9.00 ms 0.1%	0 s	> 0x3000d40c3
9.00 ms 0.1%	0 s	> 0x100028d2058

On the right side, the 'Heaviest Stack Trace' panel shows a list of memory addresses and their corresponding symbols, starting with '6997 Pharo (59234)'.

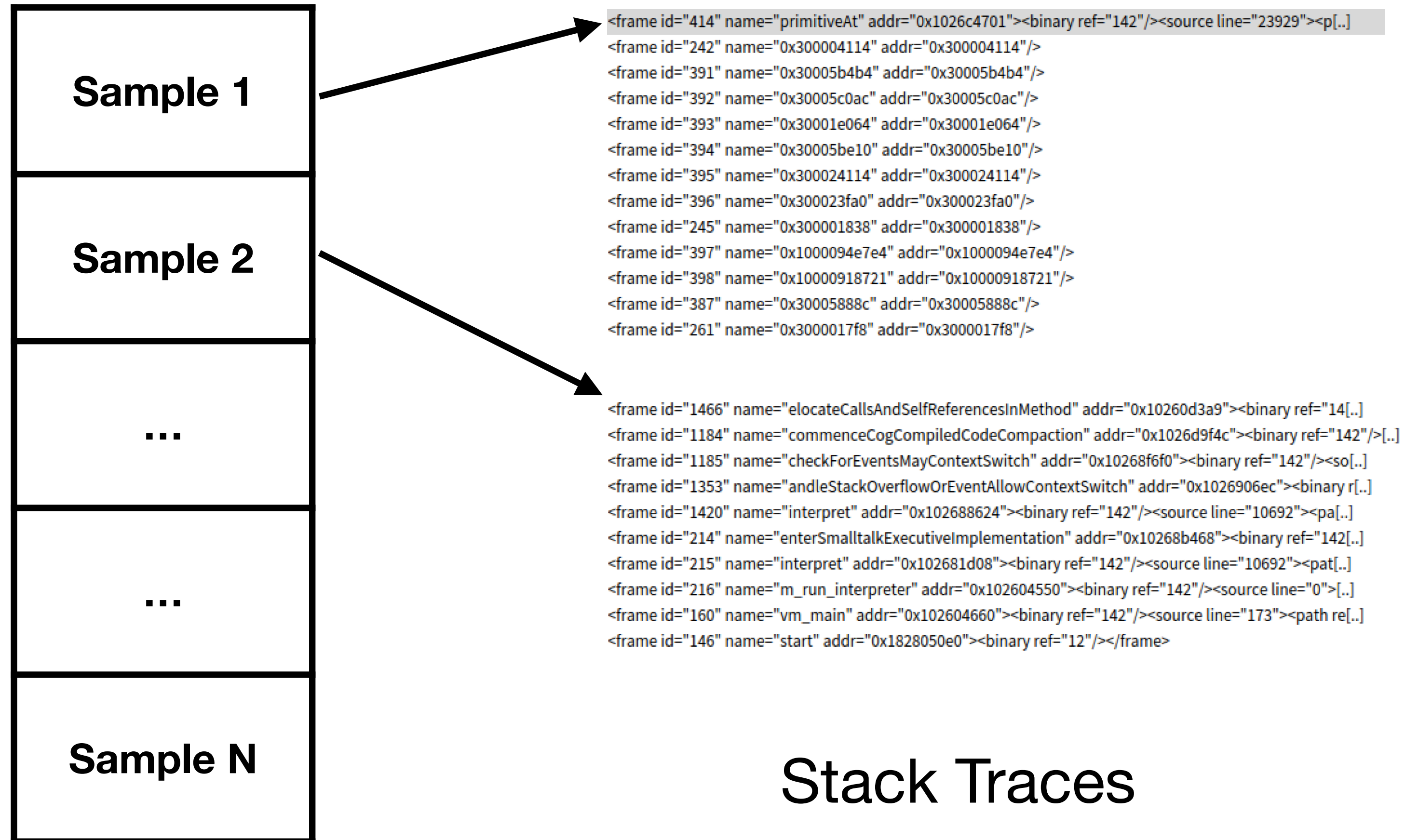
Analysing Instruments Profiles



Analyzing Samples

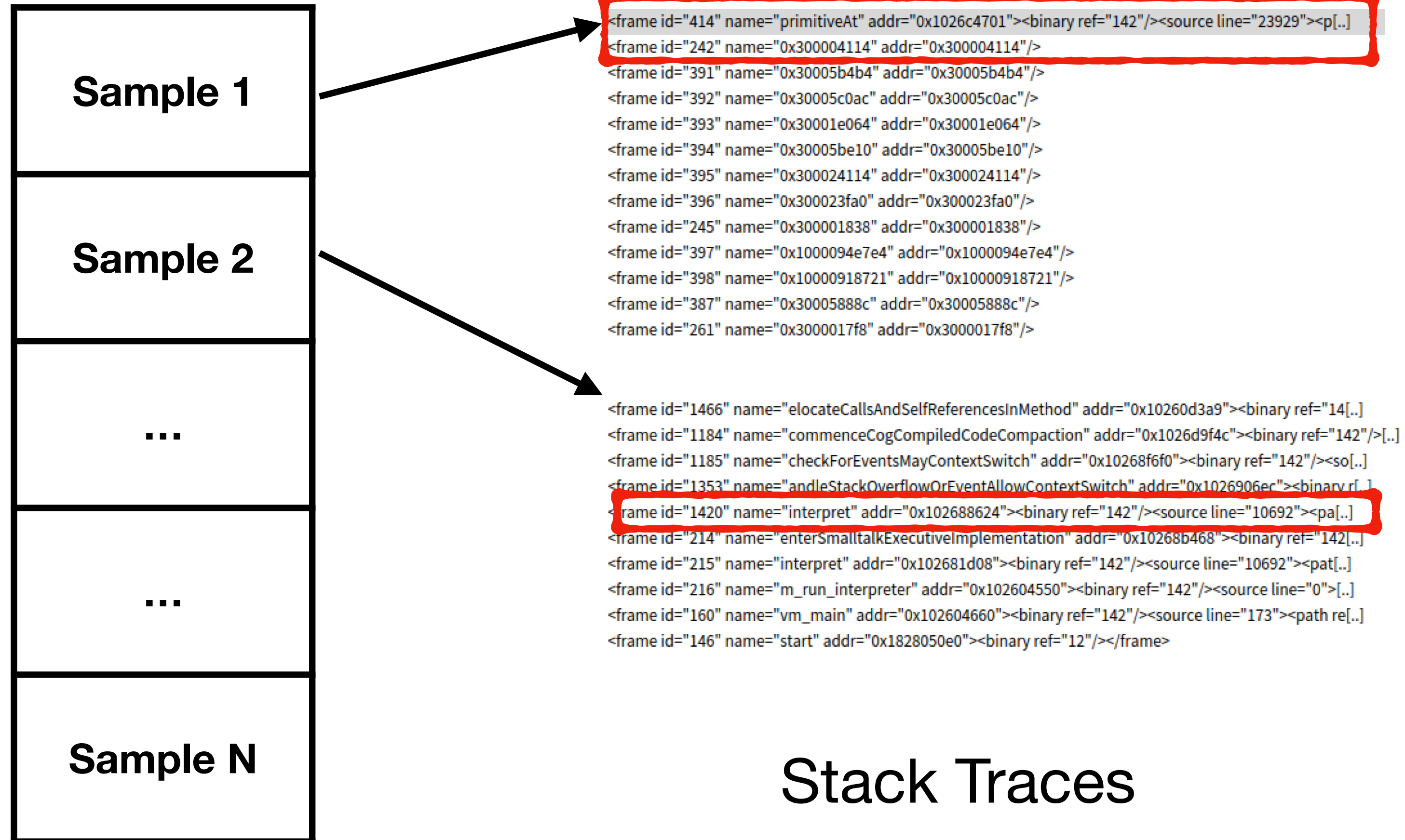


Analyzing Samples



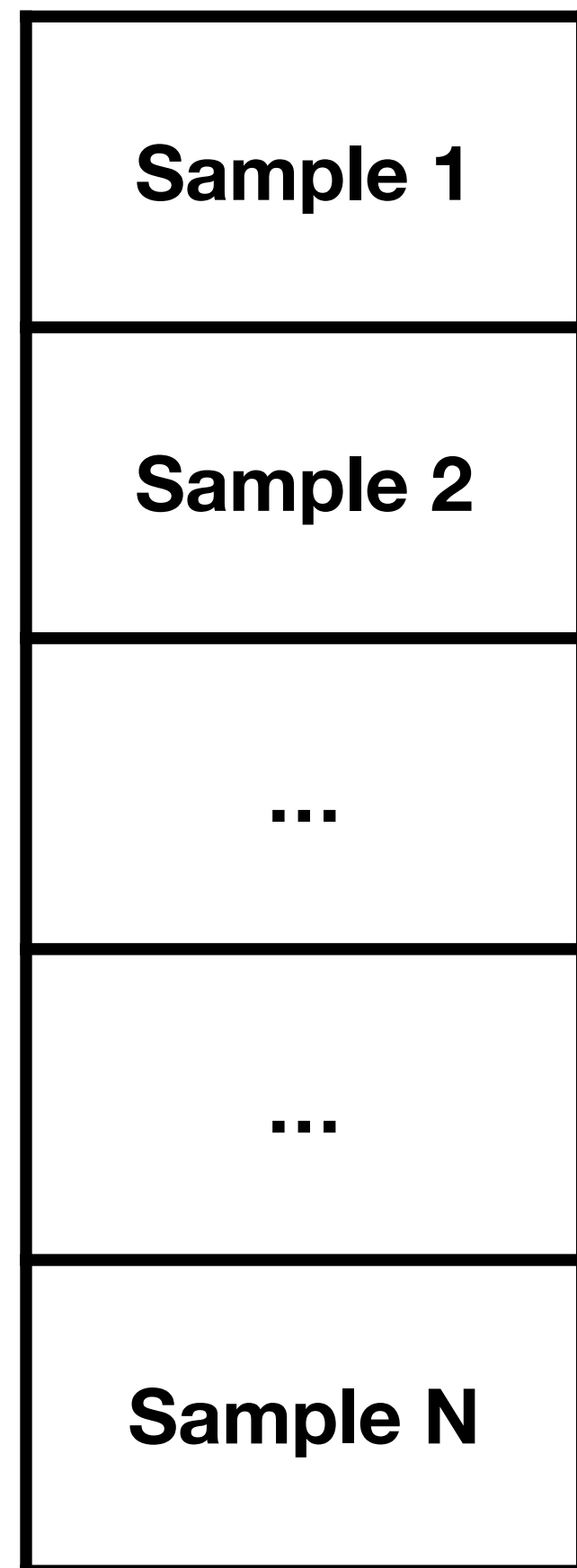
Stack Traces

Analyzing Samples



Stack Traces

Analyzing Samples



```
<frame id="414" name="primitiveAt" addr="0x1026c4701"><binary ref="142"/><source line="23929"><p[..  
<frame id="242" name="0x300004114" addr="0x300004114"/>  
<frame id="391" name="0x30005b4b4" addr="0x30005b4b4"/>  
<frame id="392" name="0x30005c0ac" addr="0x30005c0ac"/>  
<frame id="393" name="0x30001e064" addr="0x30001e064"/>  
<frame id="394" name="0x30005be10" addr="0x30005be10"/>  
<frame id="395" name="0x300024114" addr="0x300024114"/>  
<frame id="396" name="0x300023fa0" addr="0x300023fa0"/>  
<frame id="245" name="0x300001838" addr="0x300001838"/>  
<frame id="397" name="0x1000094e7e4" addr="0x1000094e7e4"/>  
<frame id="398" name="0x10000918721" addr="0x10000918721"/>  
<frame id="387" name="0x30005888c" addr="0x30005888c"/>  
<frame id="261" name="0x3000017f8" addr="0x3000017f8"/>
```

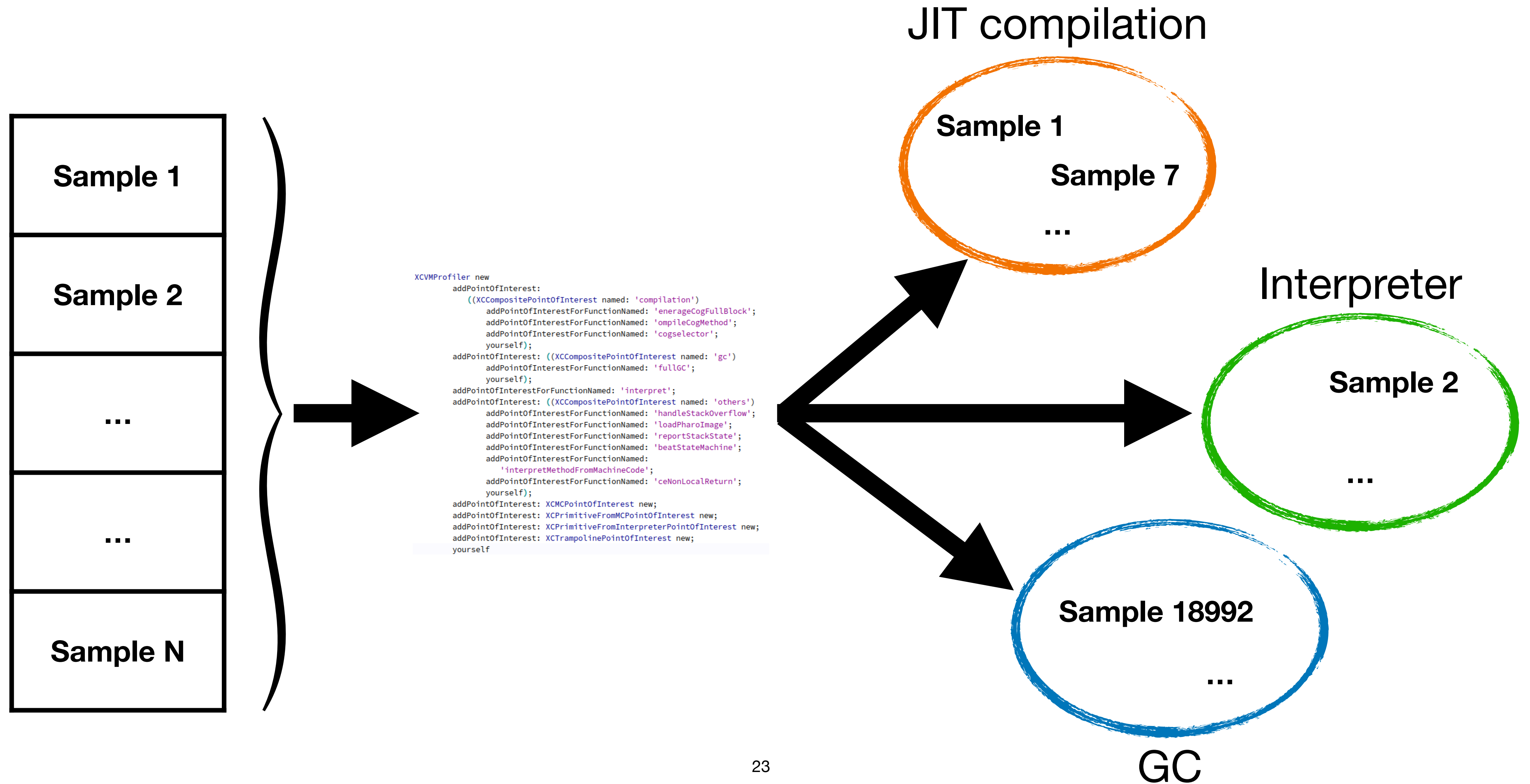
```
<frame id="1466" name="elocateCallsAndSelfReferencesInMethod" addr="0x10260d3a9"><binary ref="14[..  
<frame id="1184" name="commenceCogCompiledCodeCompaction" addr="0x1026d9f4c"><binary ref="142"/>[..  
<frame id="1185" name="checkForEventsMayContextSwitch" addr="0x10268f6f0"><binary ref="142"/><so[..  
<frame id="1353" name="andleStackOverflowOrEventAllowContextSwitch" addr="0x1026906ec"><binary r[..  
<frame id="1420" name="interpret" addr="0x102688624"><binary ref="142"/><source line="10692"><pa[..  
<frame id="214" name="enterSmalltalkExecutiveImplementation" addr="0x10268b468"><binary ref="142[..  
<frame id="215" name="interpret" addr="0x102681d08"><binary ref="142"/><source line="10692"><pat[..  
<frame id="216" name="m_run_interpreter" addr="0x102604550"><binary ref="142"/><source line="0">[..  
<frame id="160" name="vm_main" addr="0x102604660"><binary ref="142"/><source line="173"><path re[..  
<frame id="146" name="start" addr="0x1828050e0"><binary ref="12"/></frame>
```

Primitive from
Machine Code

Interpreter

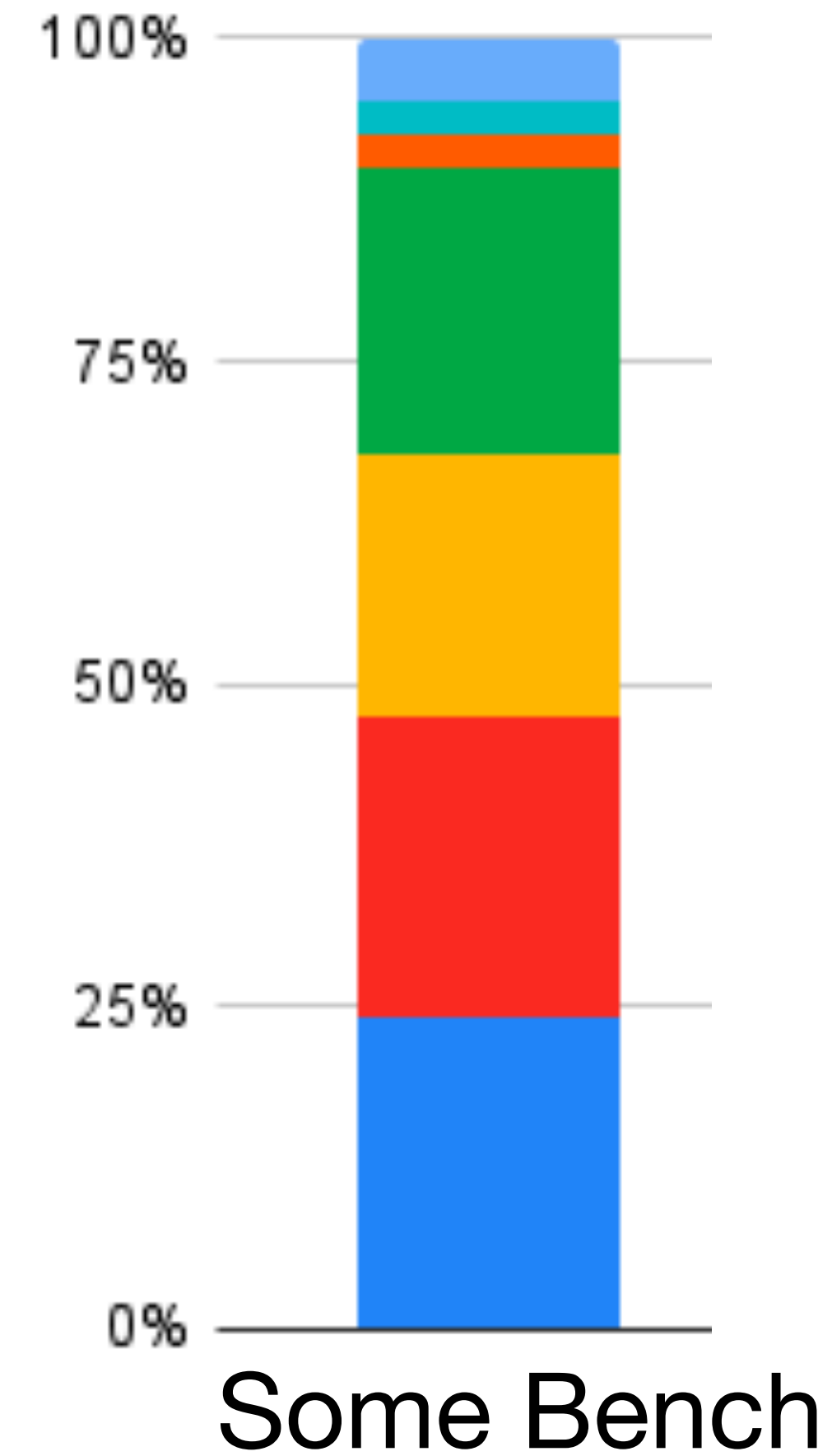
Stack Traces

Group Traces Using Heuristics



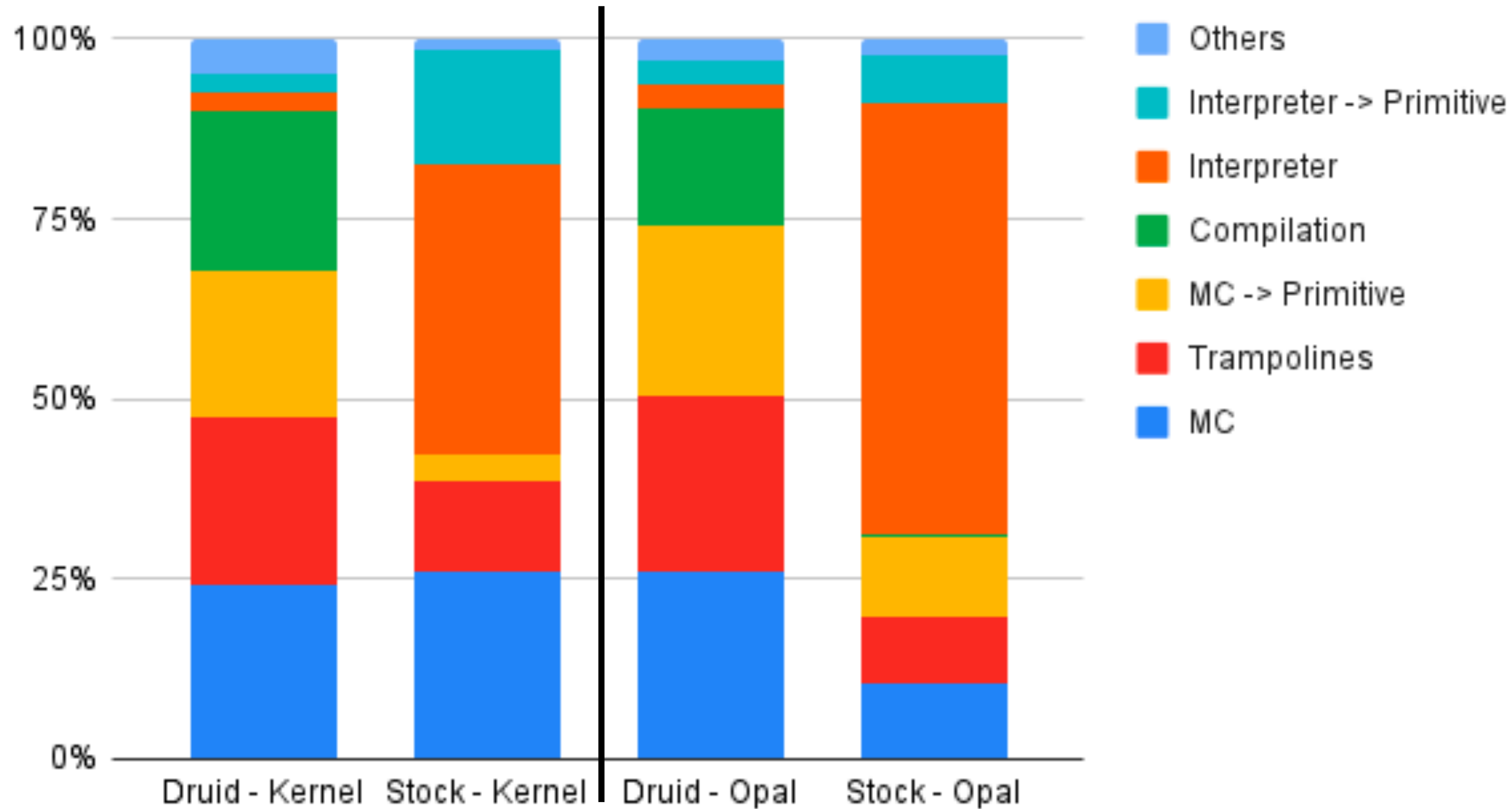
High-level VM Profile

- Time spent in
 - Interpreter
 - JIT compilation
 - JIT compiled code
 - GC
 - Primitives
 - ...



Scenario 1: Cross-JIT Profiling

Druid - Kernel, Stock - Kernel, Druid - Opal y Stock - Opal

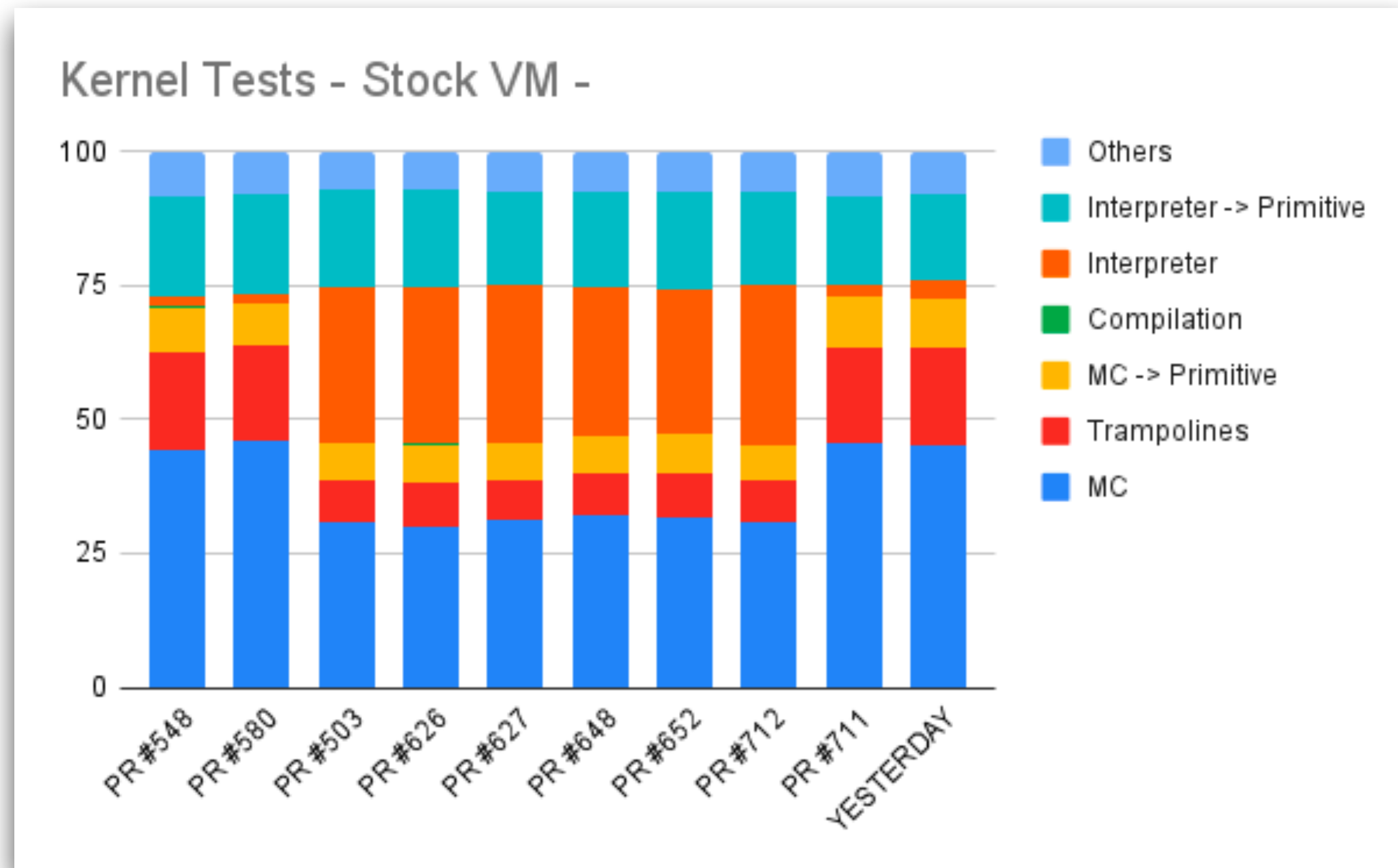


Hot Paths and Our Partial JIT Implementation

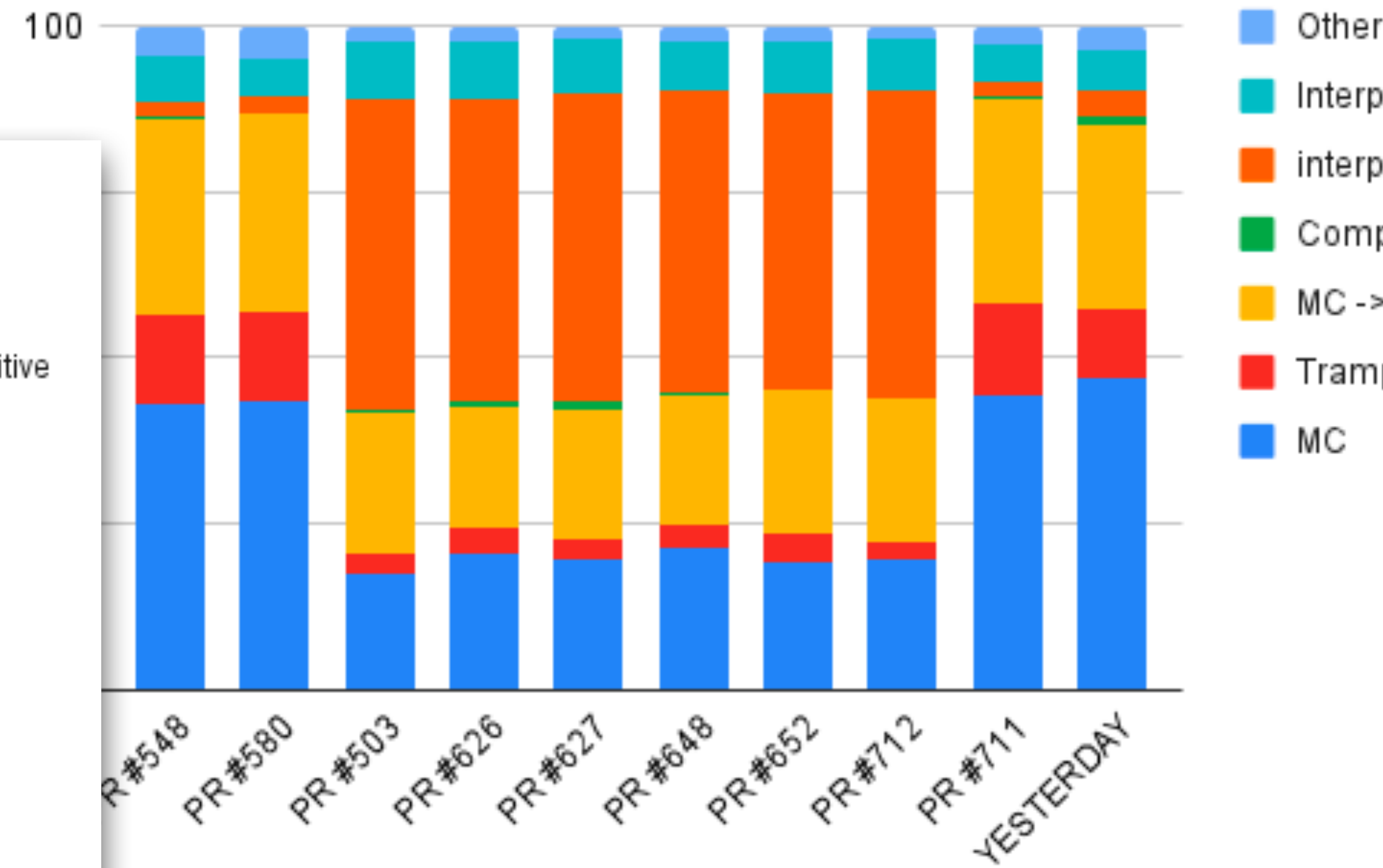
- Cogit is *all or nothing* compiler
- Hot path is not compiled!

```
value: firstArg
  "Activate the receiver, creating a closure activation (MethodContext)
  whose closure is the receiver and whose caller is the sender of this
  message. Supply the argument and copied values to the activation
  as its argument and copied temps. Primitive. Essential."
  <primitive: 207>
  | newContext |
  numArgs ~= 1 ifTrue:
    [self numArgsError: 1].
  false
  ifTrue: "Old code to simulate the closure value primitive on VMs that lack
    [newContext := self asContextWithSender: thisContext sender.
    newContext at: 1 put: firstArg.
    thisContext privSender: newContext]
  ifFalse: [self primitiveFailed]
```

Scenario 2: Cross-Version Profiling

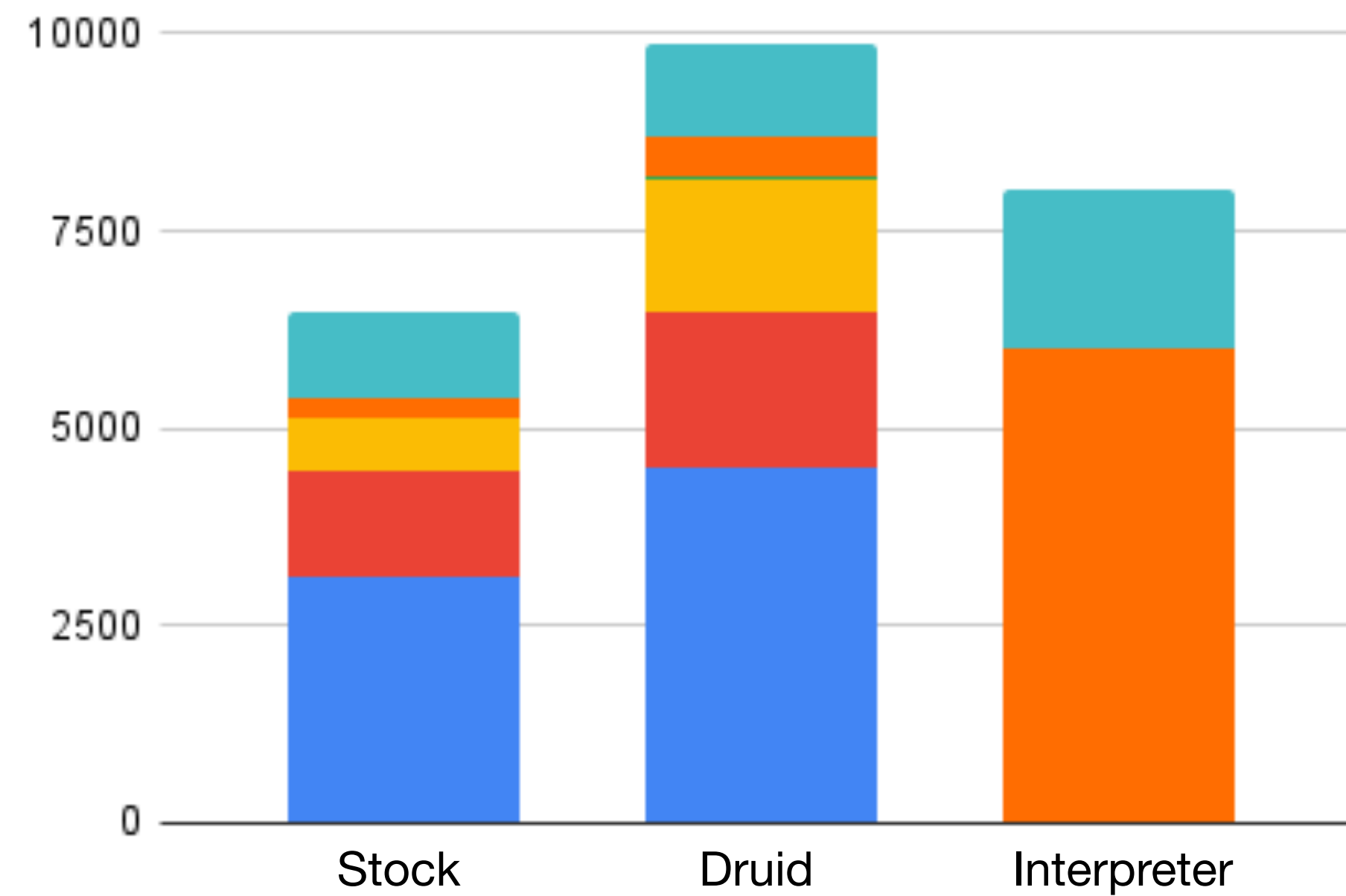


Opal Tests - Stock VM - Druid branch



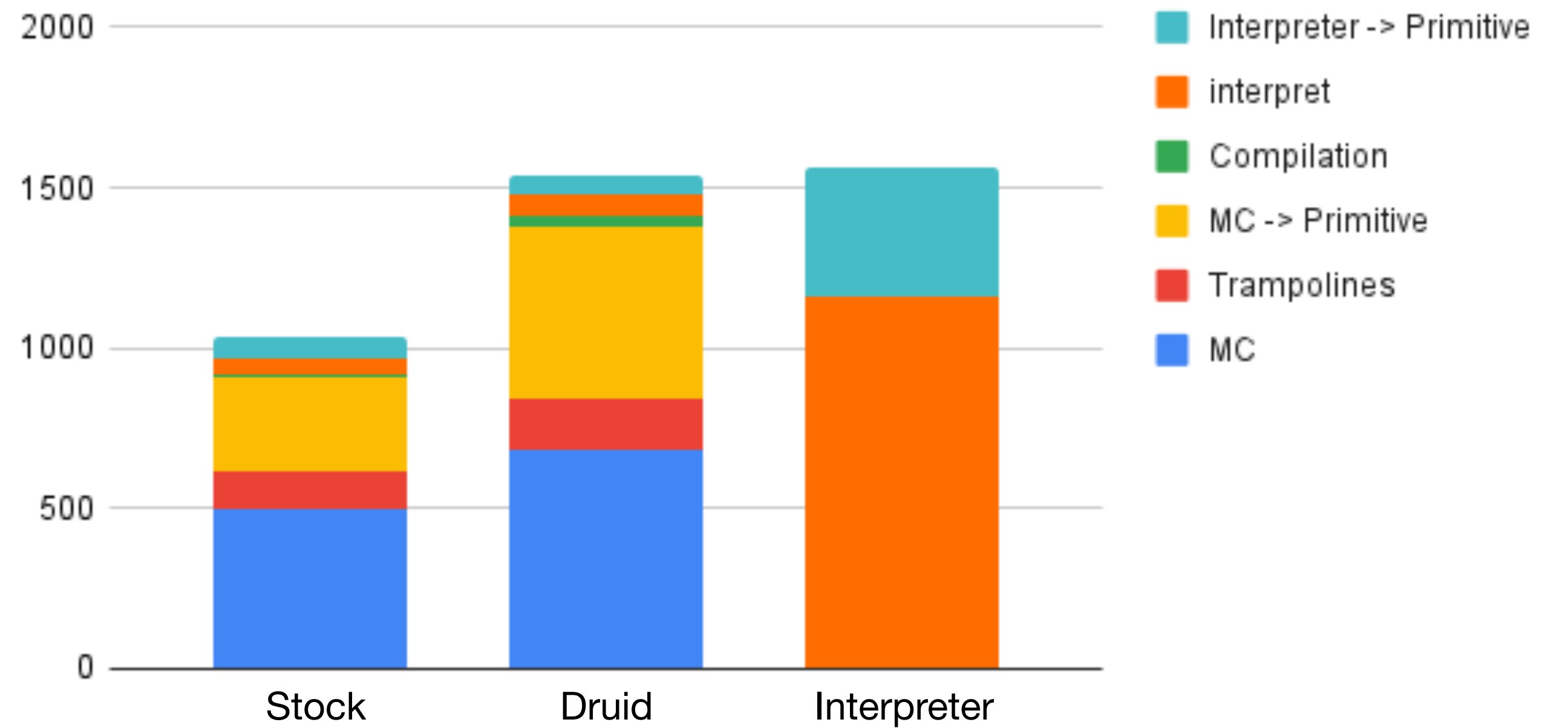
Differential Profiling + Absolute Values

Kernel Tests



Kernel - Tests

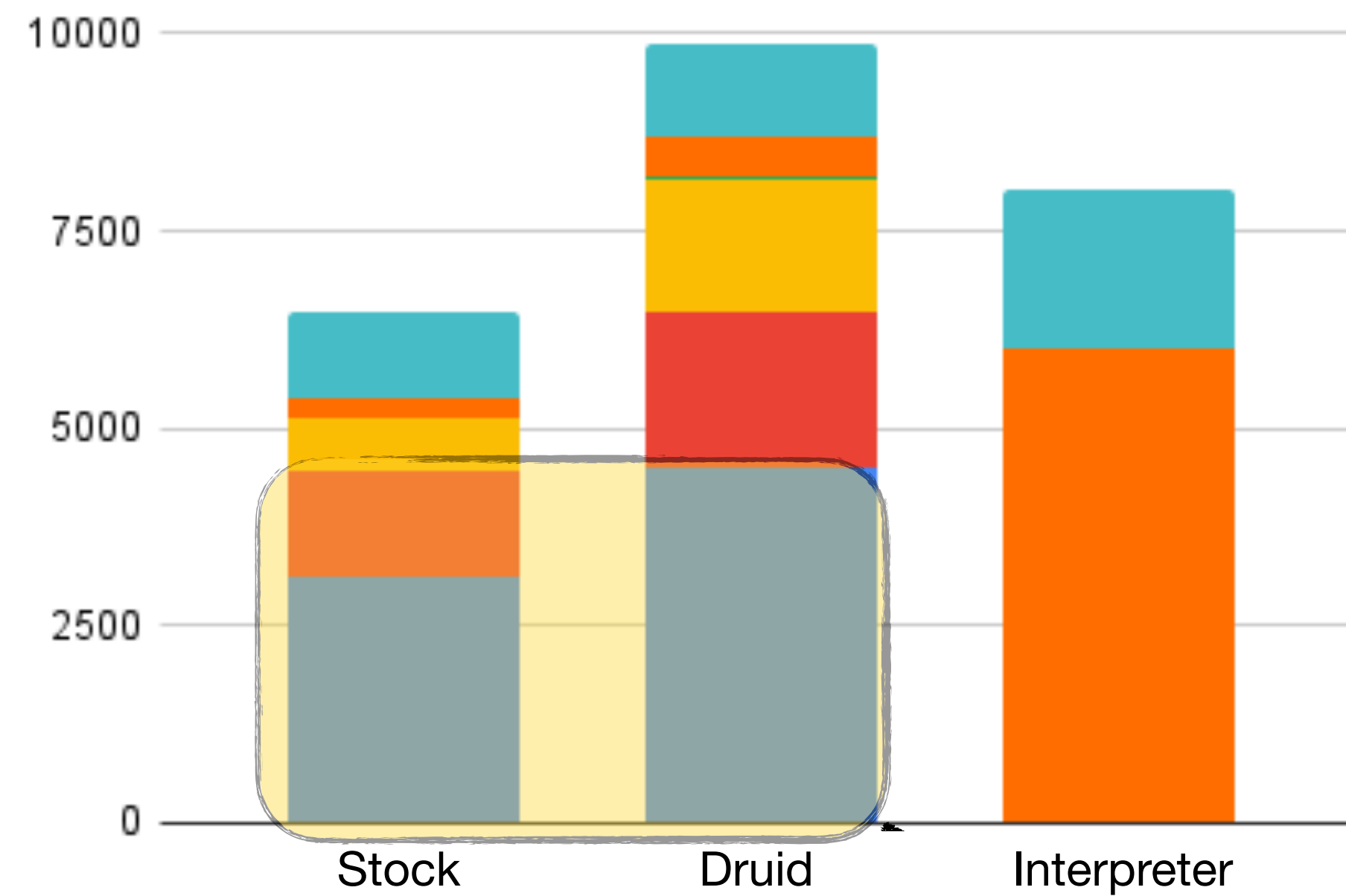
Opal Tests



Opal - Tests

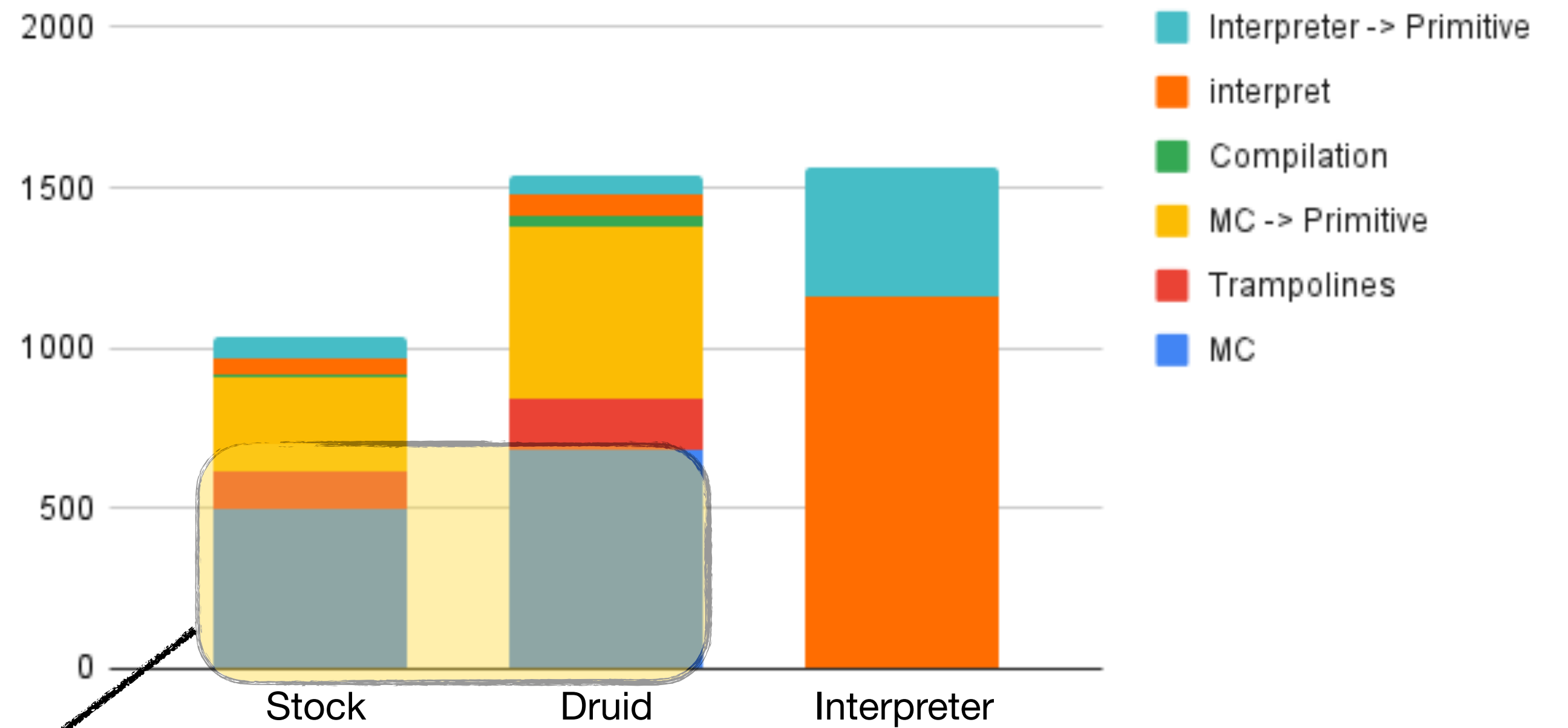
Differential Profiling + Absolute Values

Kernel Tests



Kernel - Tests

Opal Tests

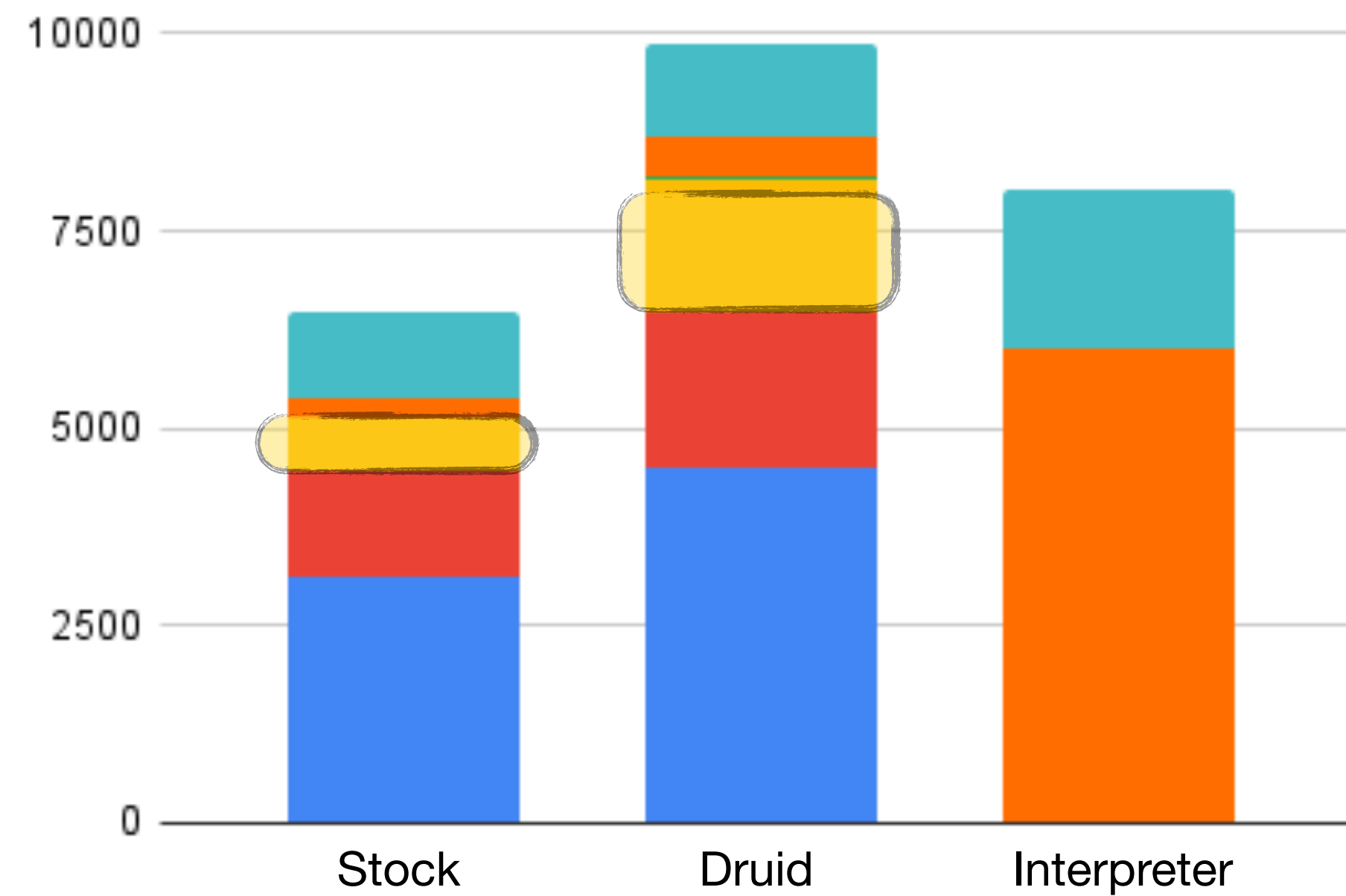


Opal - Tests

Worse Quality MC

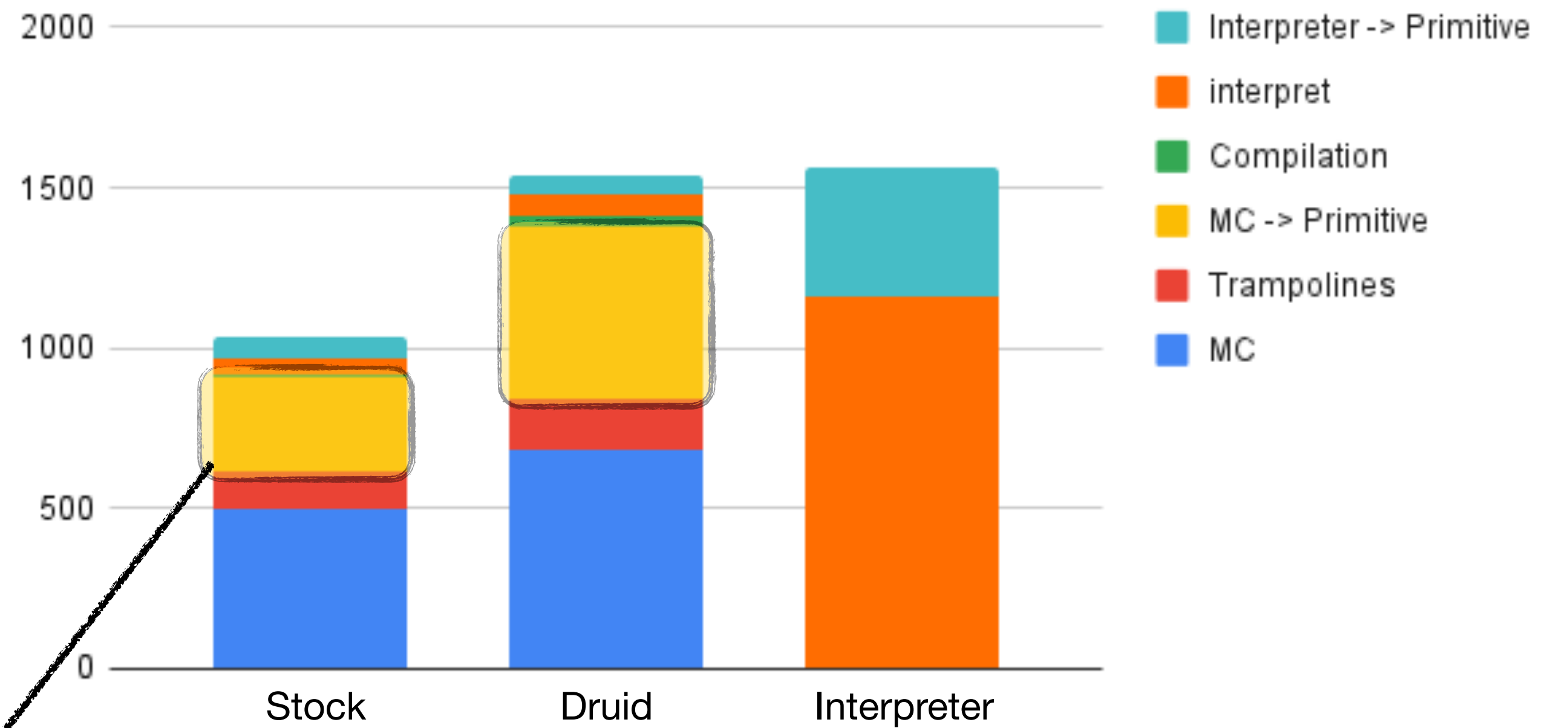
Differential Profiling

Kernel Tests



Kernel - Tests

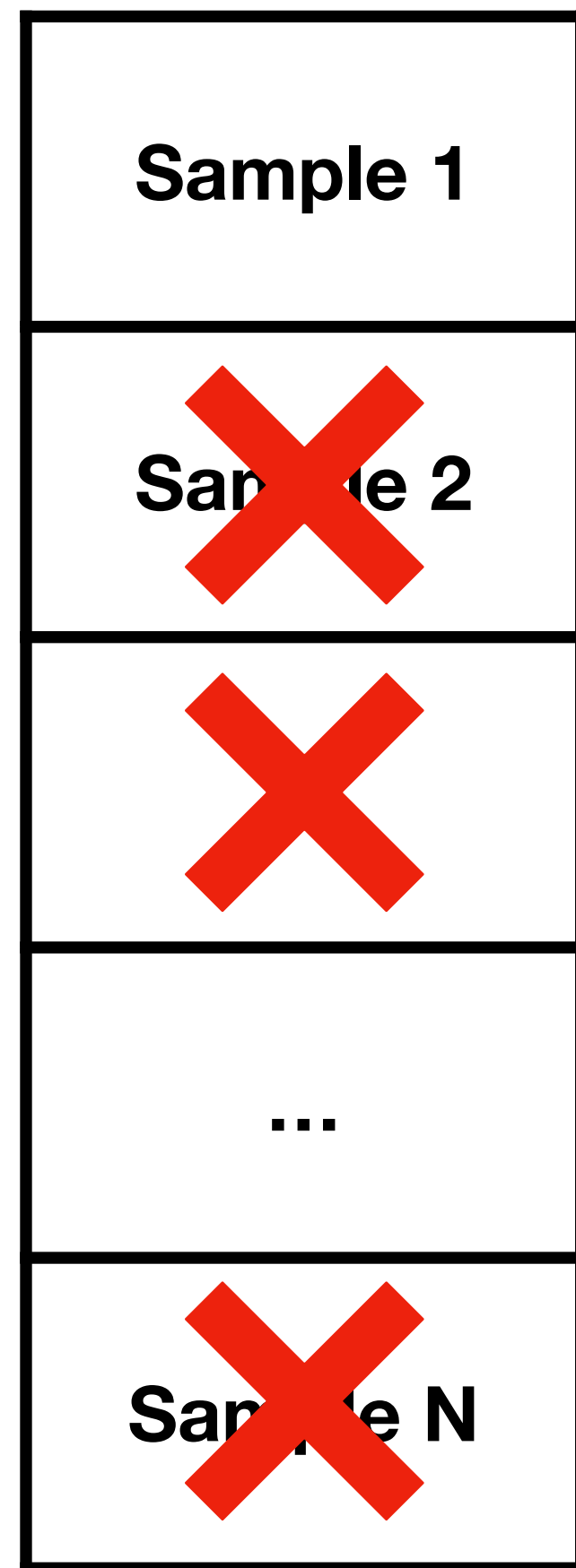
Opal Tests



Opal - Tests

More time in Primitives

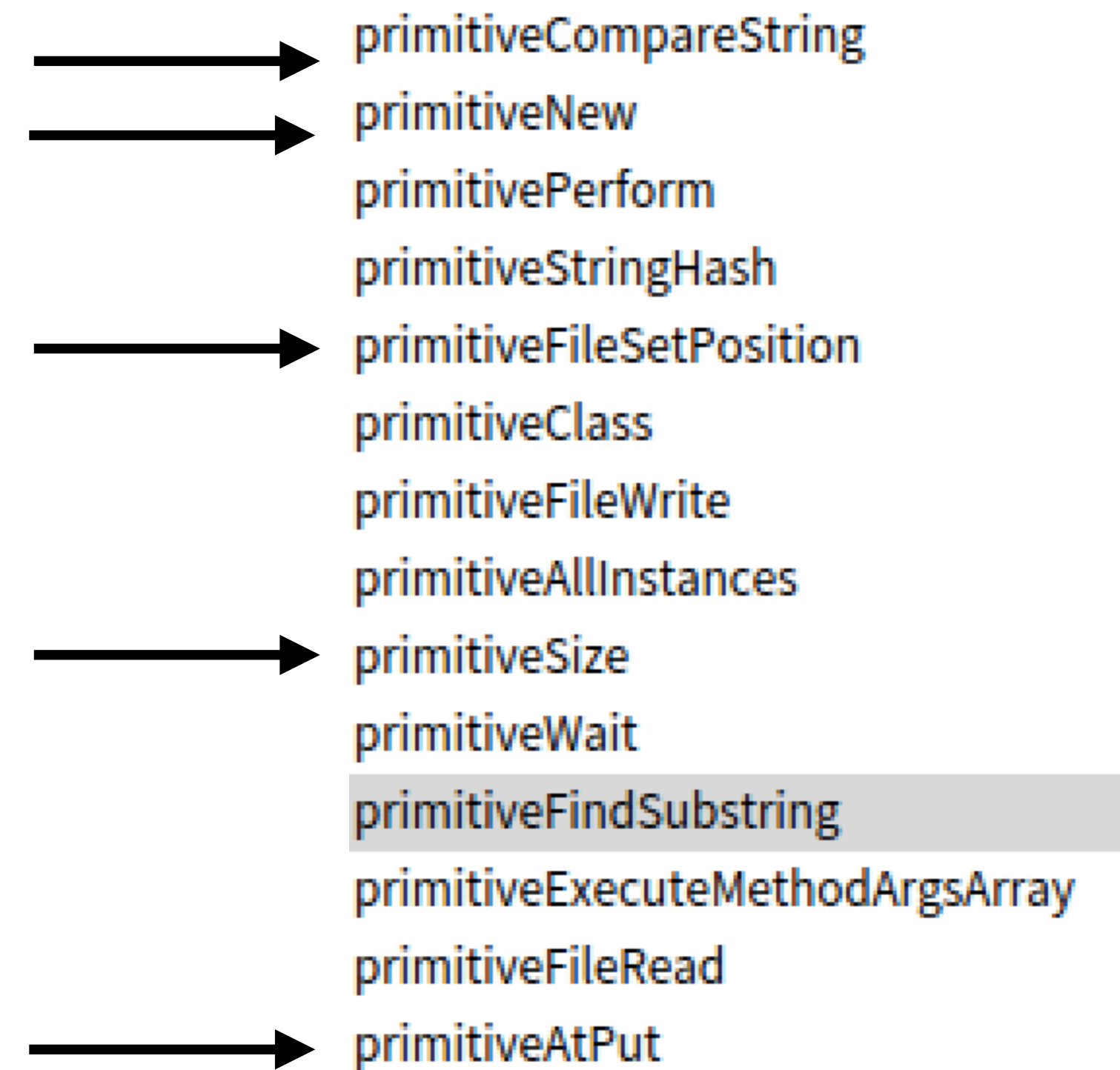
Drill-down in MC -> Primitives



Primitive Samples

```
<frame id="414" name="primitiveAt" addr="0x1026c4701"><binary ref="142"/><source line="23929"><p[...]  
</frame id="242" name="0x300004114" addr="0x300004114"/>  
</frame id="391" name="0x30005b4b4" addr="0x30005b4b4"/>  
</frame id="392" name="0x30005c0ac" addr="0x30005c0ac"/>  
</frame id="393" name="0x30001e064" addr="0x30001e064"/>  
</frame id="394" name="0x30005be10" addr="0x30005be10"/>  
</frame id="395" name="0x300024114" addr="0x300024114"/>  
</frame id="396" name="0x300023fa0" addr="0x300023fa0"/>  
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</frame id="397" name="0x1000094e7e4" addr="0x1000094e7e4"/>  
</frame id="398" name="0x10000918721" addr="0x10000918721"/>  
</frame id="387" name="0x30005888c" addr="0x30005888c"/>  
</frame id="261" name="0x3000017f8" addr="0x3000017f8"/>
```

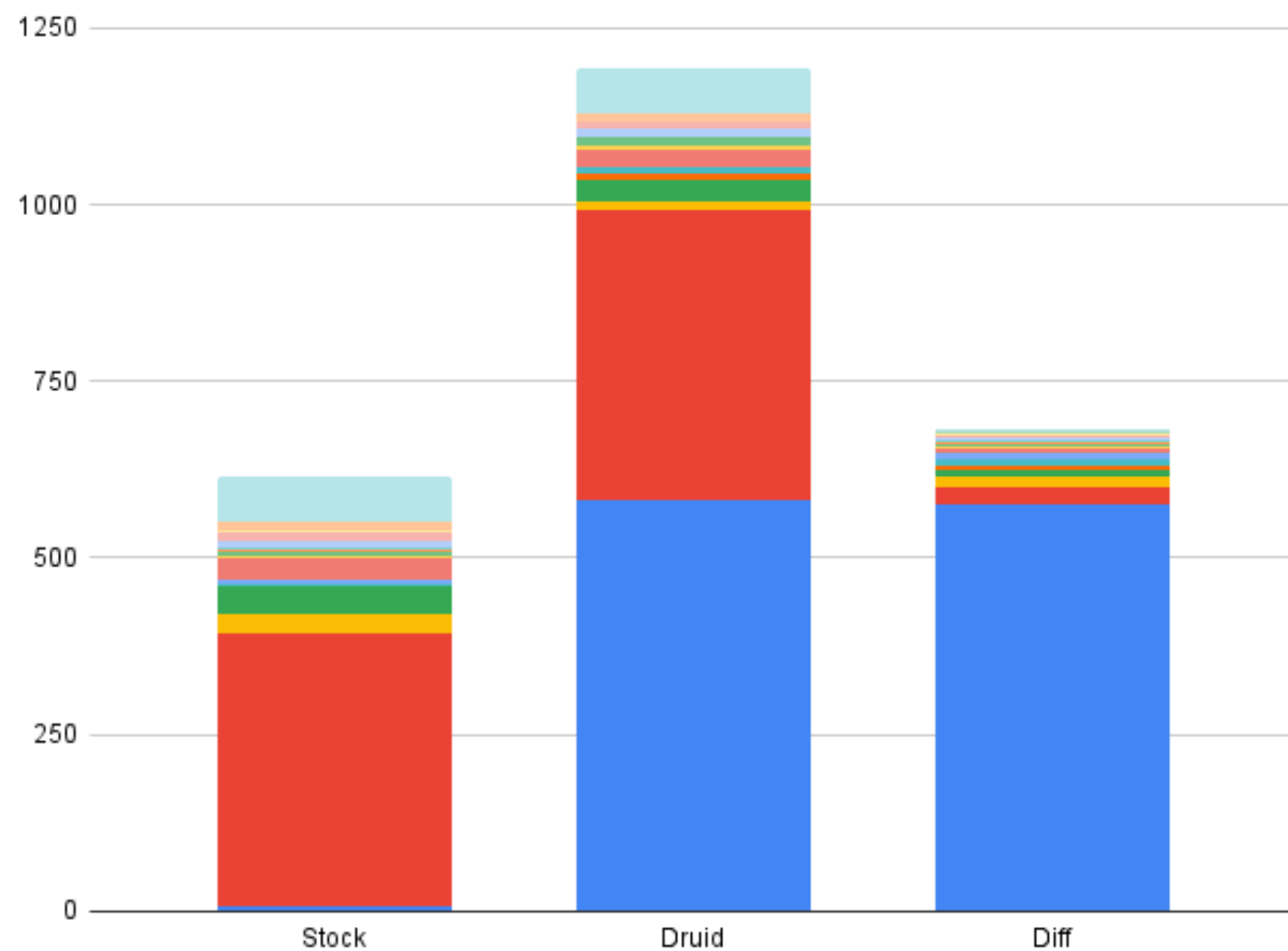
Stack Traces



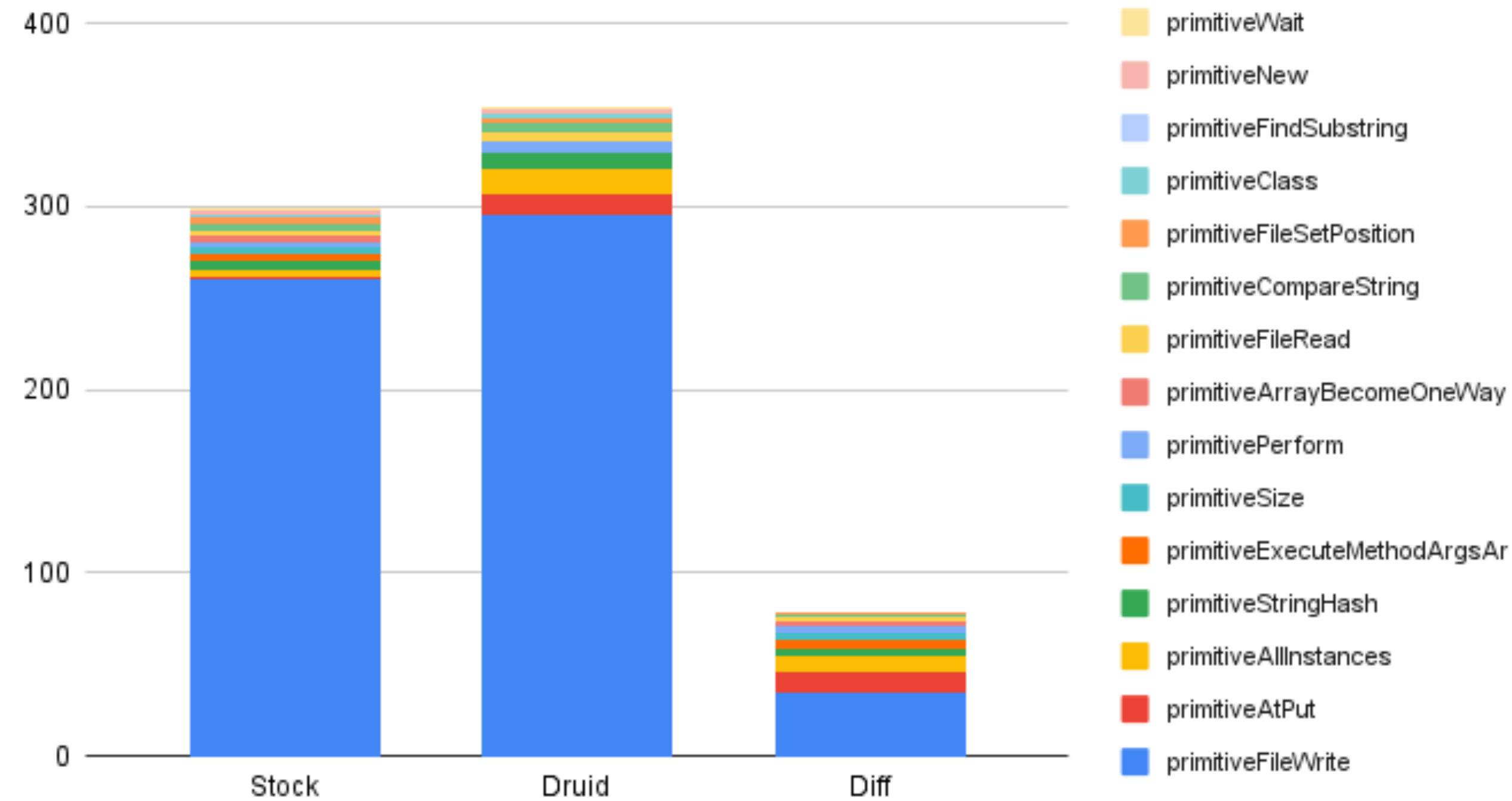
Primitives!

Differential MC->Primitive Profiling

Kernel tests -- Time spent in MC -> Primitive

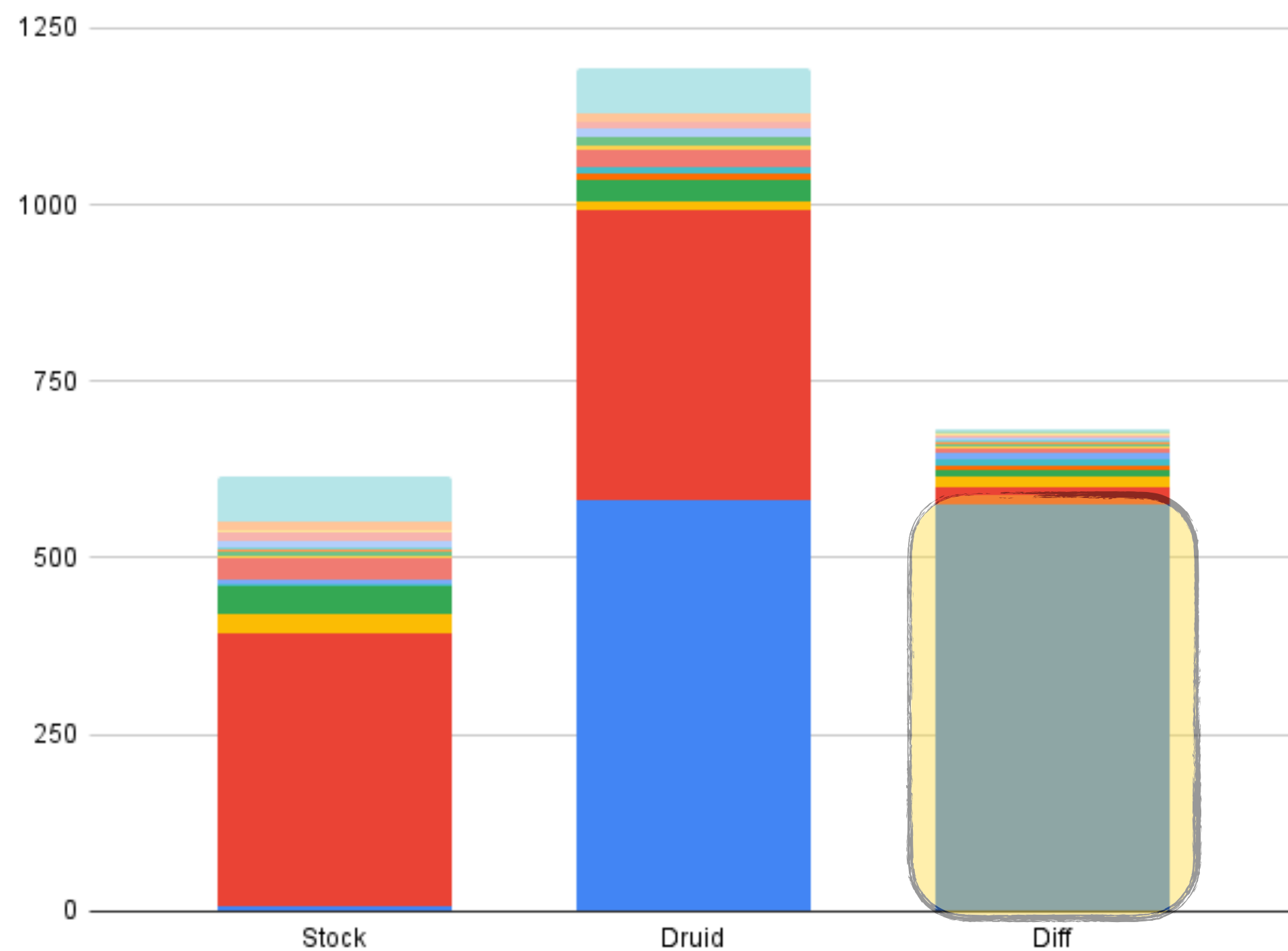


Opal tests -- Time spent in MC -> Primitive

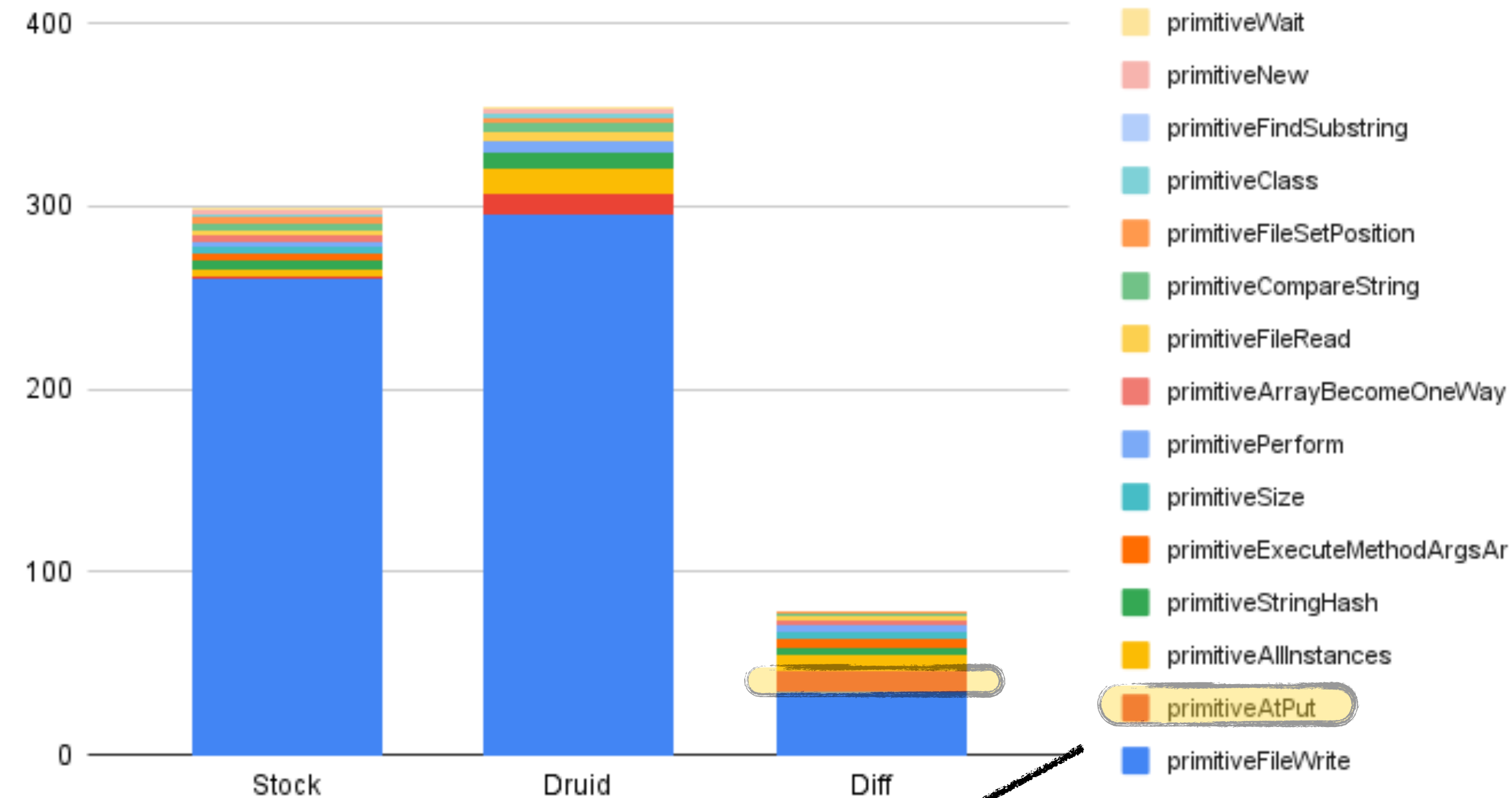


Differential MC->Primitive Profiling

Kernel tests -- Time spent in MC -> Primitive



Opal tests -- Time spent in MC -> Primitive



- primitiveMultiplyLargeIn
- primitiveStringHash
- primitiveIndexOfAsciiInS
- primitiveQuoLargeInteg
- primitiveIncrementalGC
- primitivePerform
- primitiveFindHandlerC
- primitiveArrayBecomeO
- primitiveClone
- primitiveNew
- primitiveObjectPointsTo
- primitiveAllObjects
- primitiveFullClosureVal
- primitiveStringReplace
- primitiveAllInstances
- primitiveClass
- primitiveFileWrite
- primitiveAt

- primitiveWait
- primitiveNew
- primitiveFindSubstring
- primitiveClass
- primitiveFileSetPosition
- primitiveCompareString
- primitiveFileRead
- primitiveArrayBecomeOneWay
- primitivePerform
- primitiveSize
- primitiveExecuteMethodArgsAr
- primitiveStringHash
- primitiveAllInstances
- primitiveAtPut
- primitiveFileWrite

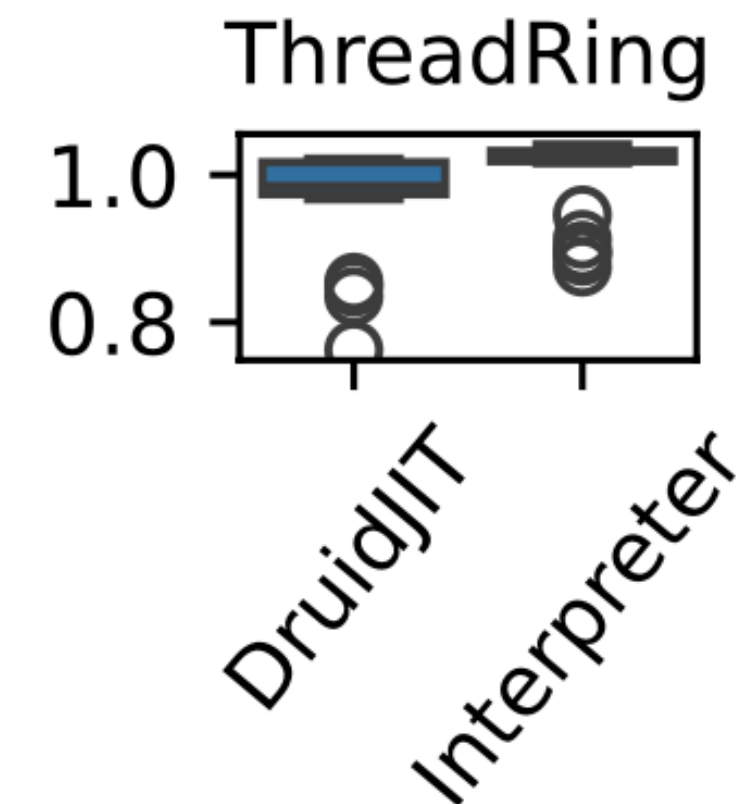
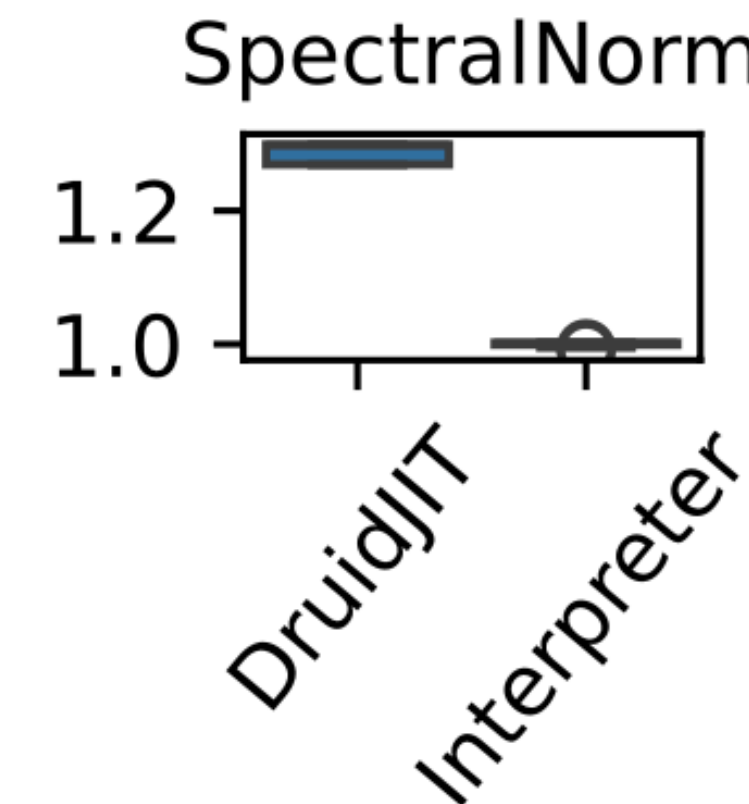
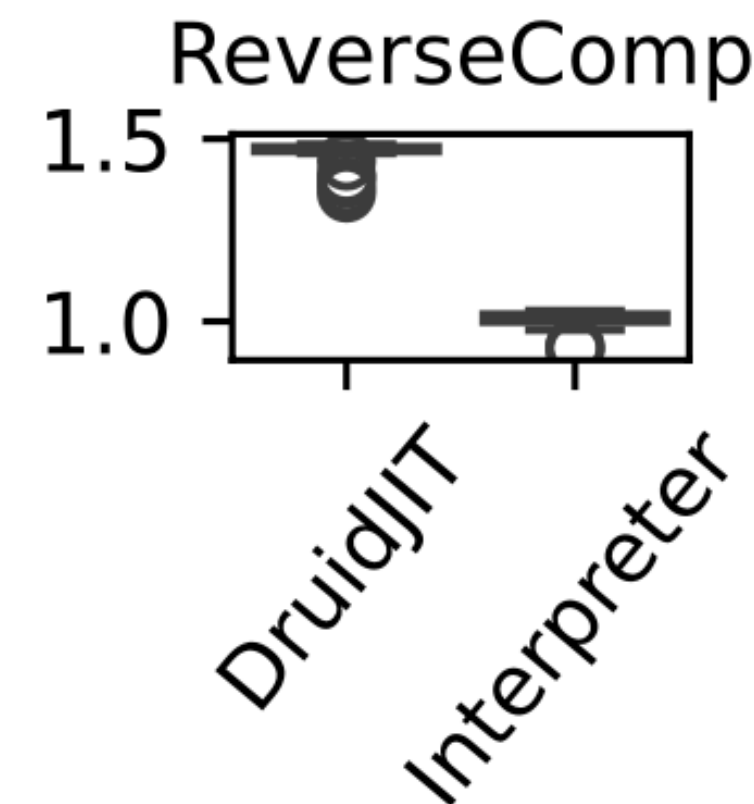
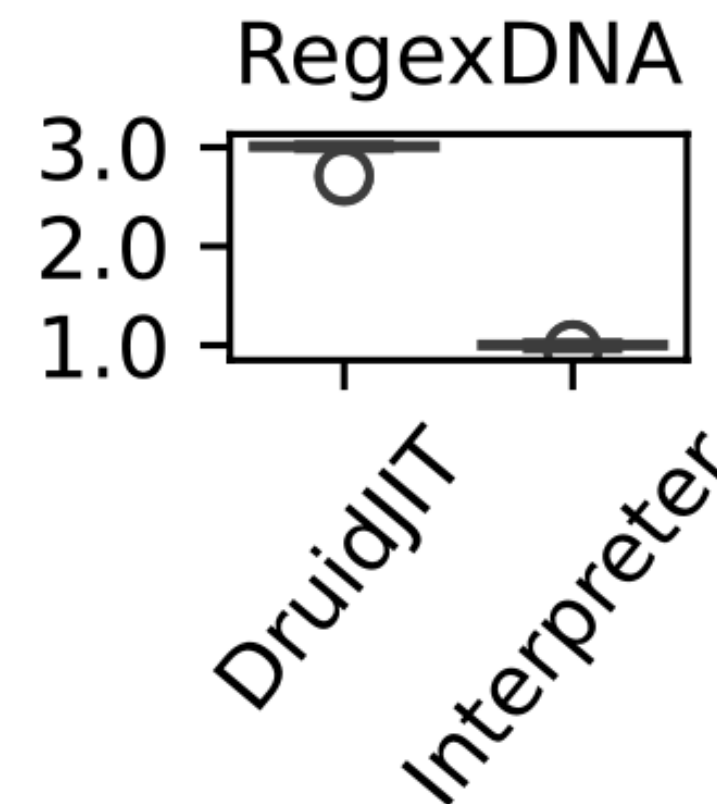
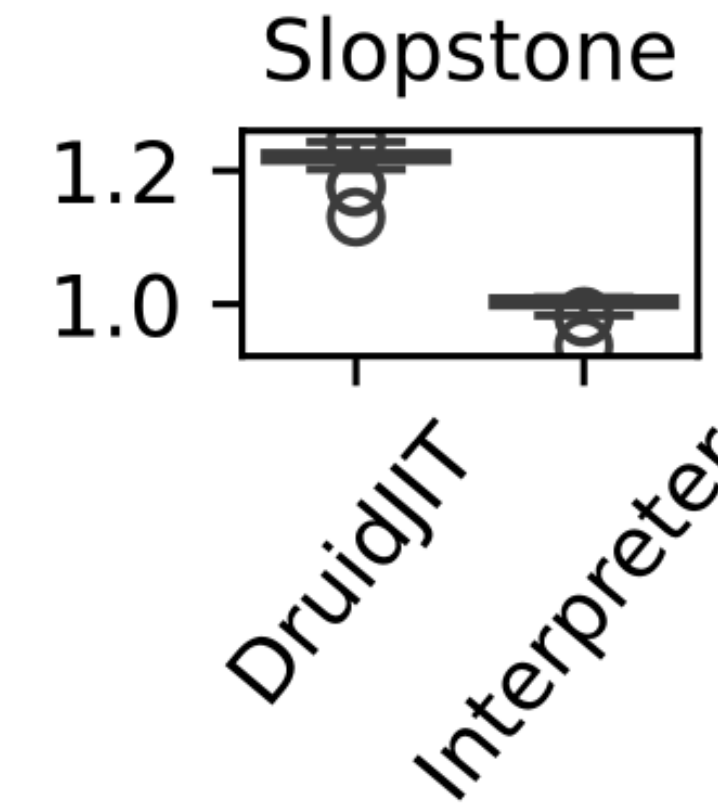
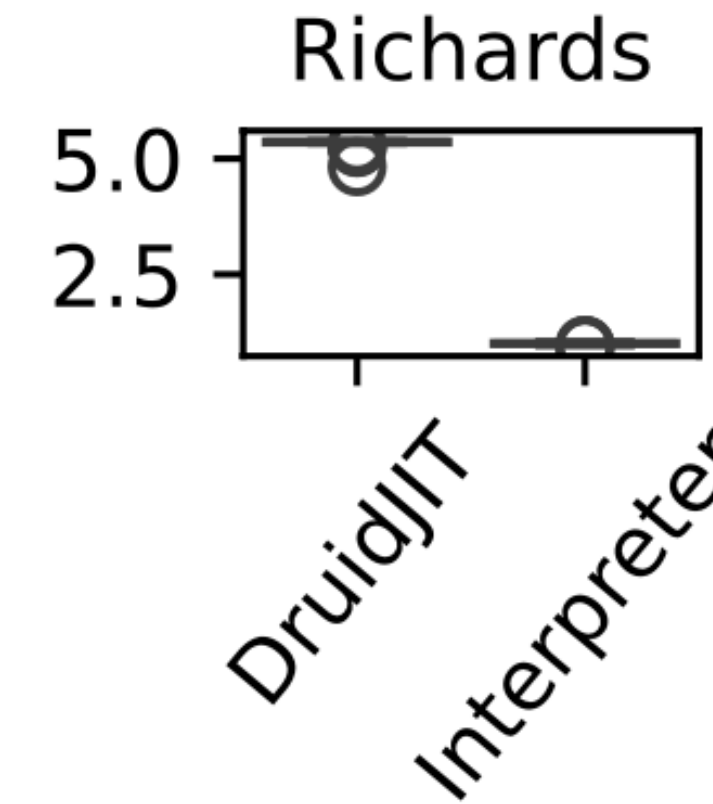
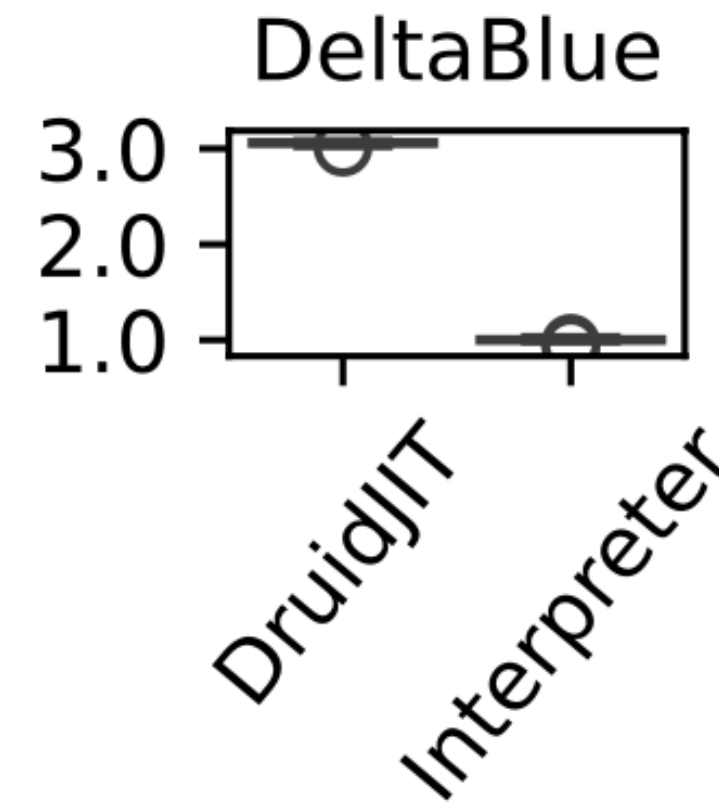
Low-hanging fruits

After Some Bit of “well-placed” Work :)

- **2x faster!**
than interpreter on avg

- Almost there:

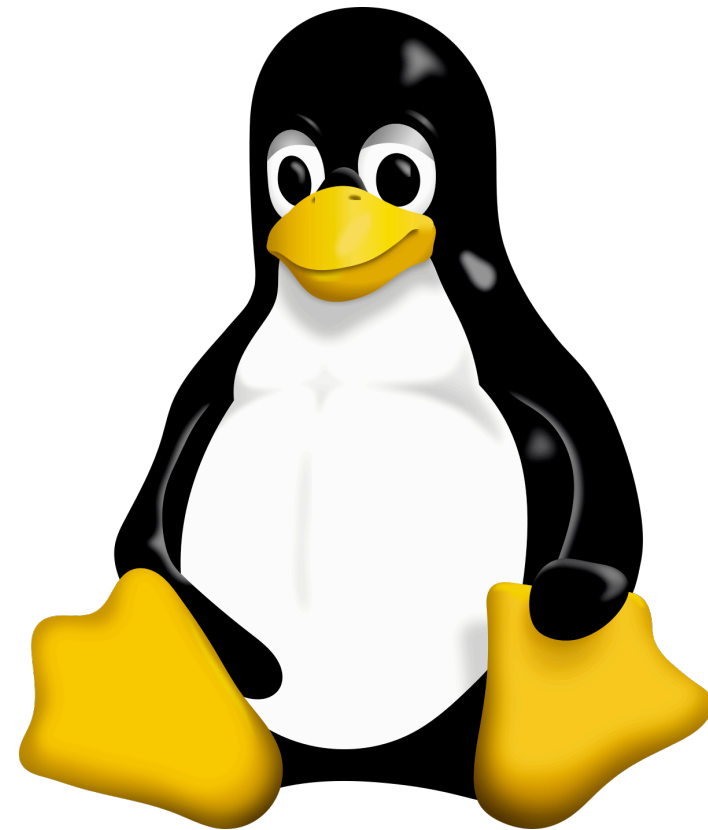
- ~0.7x manual JIT
- Missing
 - static type predictions
 - peephole optimizations on conditionals



What's next?

- **Linux integration:**

- Perf support
- *Matéo Boury*



- **Tracking Pharo's performance:**

- Performance dashboards
- Benchmark Generation
- daily, monthly, yearly



Takeaways

- Integrate with tools that do their job well (Instruments, Perf)
- Simple custom tools help debugging *complex VM scenarios*
- Tests first for good behavior
- Bench first for good performance!